IBM System z9
Introduction
October 2006

IBM System z9 Enterprise Class Update
(formerly System z9 109)
Frequently Asked Questions

Worldwide

IBM
z9 EC Update FAQ
# Table of Contents

Announcement Overview .......................................................... 3
Community ................................................................. 8
z9 EC Hardware .............................................................. 9
z9 EC Upgradeability .......................................................... 12
Availability Enhancements ....................................................... 14
IFL, zAAP and zIIP specialty engines ........................................ 17
Clarification on a few DB2 terms for better zIIP understanding ............ 24
z9 EC Pricing ................................................................. 26
Performance ................................................................. 29
Networking Enhancements ..................................................... 32
I/O Enhancements ............................................................ 34
On/Off Capacity on Demand (On/Off CoD) and CBU ......................... 38
Cryptographic Enhancements .................................................. 42
Parallel Sysplex Enhancements ................................................. 50
GDPS Enhancements .......................................................... 51
Announcement Overview – z/VSE .............................................. 52
Announcement Overview – z/VM ............................................... 54
IBM Global Financing (IGF) ..................................................... 56
Fiber Cabling for your IBM System z9 ......................................... 58
Statements of Direction .......................................................... 61
Announcement Overview

**Question:**
What was announced April 27, 2006?

**Answer:**
IBM announced extensions to the IBM System z9™ family. First, significant new capabilities were announced for the System z9 109 (z9-109), henceforth known as the IBM System z9 Enterprise Class (z9 EC). These capabilities include subcapacity versions, delivered through 24 new capacity settings, and the general availability of a new specialty engine for data serving and data mining, the System z9 Integrated Information Processor (zIIP). Second, following on the success and market acceptance of the z9-109 high end offering, IBM introduced a new midrange mainframe server, the IBM System z9 Business Class (z9 BC). The z9 BC is available in two models, one with a low entry point of 26 MIPS. Both models provide a wide choice of capacity settings and full support of all mainframe specialty engines: Integrated Facility for Linux® (IFLs), Internal Coupling Facilities (ICFs), IBM System z™ Application Assist Processors (zAAPs) and zIIPs, giving clients flexibility and choice in both initial deployment and future growth.

**Question:**
What was announced October 10, 2006?

**Answer:**
IBM announced Server Time Protocol (STP) with planned availability of January 31, 2007. The STP feature is designed to allow events occurring in different z9 EC, z9 BC, z990 and z890 servers and Coupling Facilities (CFs), to be properly sequenced in time. STP is designed to reduce or eliminate the requirement for Sysplex Timers. STP supports a multisite sysplex up to 100 km (62 miles) without requiring an intermediate site. Previously, an intermediate site was recommended to locate one of the Sysplex Timers, when the multisite sysplex distance exceeded 40 km (25 miles). For more information on STP please look at the Web site: www.ibm.com/systems/z/ps0/stp.html. For FAQs on STP please review the Web site www.ibm.com/systems/z/faq/. IBM also announced a new feature that will enable the System z9 Business Class (z9 BC) to operate on a non raised floor.

**Question:**
Why was the System z9 naming changed with the April 2006 announcement?

**Answer:**
Fundamentally, the naming approach for the System z9 product line or family is the same. The key element of the name remains the same, System z9 (or IBM System z when referring to the mainframe class or family of products including System z9 and IBM eServer™ zSeries® etc.). To accommodate the introduction of different System z9 offerings, what has changed is the means of identifying the particular family member. Previously, the only family member available was a high-end offering, System z9 109 (z9-109), which is now known as the System z9 Enterprise Class (z9 EC) going forward. The new member of the System z9 family for the midrange will be
known as the System z9 Business Class (z9 BC). This change was ultimately made so that the different products now have a more descriptive term that helps position them to our clients.

**Question:**
What is the machine number of the z9 EC?

**Answer:**
The machine number has not changed from the z9-109. It will still be 2094.

**Question:**
What impact did the name change have for existing System z9 109 customers?

**Answer:**
Our customers should have no impact as a result of this change. The process for these customers to upgrade to take advantage of the capabilities of the System z9 Enterprise Class are not affected by the change in name, except that documentation may now be listed for the “System z9 EC formerly known as z9-109”. Redirects will be employed for key Web content featuring the z9-109 so that customers can have easy access to relevant information.

**Question:**
How does the Mainframe Charter and the October 2004 announcements influence the System z9 announcements?

**Answer:**
The Mainframe Charter, announced in August 22, 2003, provided a framework for planned future investment and highlighted IBM’s intentions for delivering ongoing value to our mainframe customers.

The introduction to the market of the IBM System z9 109 (z9-109) in July 2005 and enhancements now available on the z9 EC have brought significant technology advances in the areas of performance, scalability, availability, security and virtualization. The April announcement, which included the newest member of the family, the z9 BC, extended the System z9 family to provide our latest technology to our small and medium mainframe customers.

In addition to the innovation of the z9 EC and z9 BC, we also announced additional price / performance improvements and significant advances in our drive to increase mainframe skills and broaden the mainframe community. These changes will be covered later in the Community and Pricing section of the document.

**Question:**
How was the z9 EC improved on the System z9 109 that was announced July 26, 2005?

**Answer:**
The July 26, 2005 announcement of the IBM System z9 109 (z9-109) brought significant technology advances to deliver on our mainframe strategy, while also strengthening its leadership in enterprise computing. These leadership capabilities in areas such as scalability, availability, security and resiliency, intelligent workload management, integration and virtualization made the z9-109 an ideal platform for supporting central corporate databases and
mission-critical enterprise-wide applications. With the z9 EC, we have strengthened and continued that leadership as we introduce additional capacity settings with subcapacity general purpose processors (CPs), announced the hardware availability of our IBM System z9 Integrated Information Processor (IBM zIIP), and continued to help improve our FICON® performance and throughput.

Listening to our customers, and realizing their requirement for more granular capacity settings within the z9-109, the z9 EC offers subcapacity general purpose processors with a larger variety of capacity choices to meet their business needs.

In January 2006 we announced the latest specialty engine – the zIIP. The zIIP, when combined with the enabling PTFs for z/OS® 1.6 and DB2 Universal Database™ (UDB) for z/OS V8, is designed to help improve resource utilization and lower the cost of eligible workloads, enhancing the role of the z9 EC as the data hub of the enterprise.

The z9 EC now includes FICON Express4 for potential improved I/O capacity and performance with the next generation of FICON/FCP. This feature may offer increased channel aggregation and shorter backup windows to help reduce the cost of storage operations and infrastructure.

**Question:**
Who would be interested in the z9 EC with subcapacity processors?

**Answer:**
Offered on our IBM S/390® and zSeries midrange servers since 1999, subcapacity processors have allowed customers to choose a server sized to best meet business requirements. Smaller incremental steps between capacity settings can allow customers to manage their growth as well as their costs, in smaller increments. On the z9 EC, subcapacity processors are available on servers with eight or fewer general purpose processors. With the z9 EC four book design, additional available processors within the server can be characterized as specialty engines (ICFs, IFLs, zAAPs or zIIPs) or can be used for Capacity Backup (CBU) capacity.

This new design means that customers will now have the option of selecting a server that meets both total capacity requirements as well as being able to select the number of general purpose processors that best fits their application and processing requirements.

**Question:**
What are the planned availability dates of the enhancements announced on April 27th and October 10th?

**Answer:**
The 24 capacity settings of the z9 EC Models, the zIIP, and FICON Express4 are all available at this time. The non raised floor capability on the z9 BC will be available November 17, 2006. STP will be available on January 31, 2007.
**Question:**
What software is supported on the z9 EC?

**Answer:**
The following table provides the minimum OS support levels for the z9 EC.

| OS Software | ▪ z/OS  
|            | ▪ z/VM®  
|            | ▪ Linux on System z9 (64-bit and 31-bit distribution)  
|            | ▪ z/VSE™  
|            | ▪ VSE/ESA™  
|            | ▪ TPF  
|            | ▪ z/OS 1.4 and subsequent releases*  
|            | ▪ z/VM 4.4 and subsequent releases*  
|            | ▪ Red Hat RHEL 4, SUSE SLES 9  
|            | ▪ z/VSE 3.1  
|            | ▪ VSE/ESA 2.7*  
|            | ▪ TPF 4.1 and z/TPF 1.1  

* Note: Support for z/OS V1.4 and 1.5 will end on March 31, 2007. Support for z/VM V4.4 will end September 30, 2006. Support for VSE 2.7 will end February 28, 2007.
Question:
Where can I find the FAQs on the z9 BC and STP?

Answer:
The URL: www.ibm.com/systems/z/faq/ that carried this FAQ list will also have the z9 BC and STP FAQs.

Question:
Where can I find the data sheets for the System z9 EC and z9 BC?

Answer:
The URLs are as follows:

z9 BC Data sheet
ibm.com/common/ssi/fcgi-bin/ssialias?infotype=pm&subtype=sp&pdf=yes&appname=STG_ZS_USEN&htmlfid=ZSD01858USEN

z9 EC Data sheet
ibm.com/common/ssi/fcgi-bin/ssialias?infotype=pm&subtype=sp&pdf=yes&appname=STG_ZS_USEN&htmlfid=ZSD01096USEN

z/VM 5.2 Data sheet
ibm.com/common/ssi/fcgi-bin/ssialias?infotype=pm&subtype=sp&pdf=yes&appname=SS_ZS_USEN&htmlfid=ZSD01145USEN.
Question: 
I know the IBM System z continues to speak of progress in building a mainframe community. What is the progress in mainframe skills growth?

Answer:
Significant progress has been made toward our goal of adding 20,000 new mainframe skilled professionals in the marketplace by 2010. The IBM Academic Initiative has helped educate 10,000 skilled students to date. Our focus is to continue to add active mainframe curriculums to schools around the globe – over 230 schools are enrolled today – and foster partnering between schools and customers to not only build, but place skills. The program is thriving as evidenced by the increase in courseware, access by schools to the university hubs, growing university ambassadors and participating professors and schools. For more information, go to ibm.com/servers/eserver/zseries/about/charter/community.html

Question:
How can I ensure my mainframe personnel have the skills they need and the right learning roadmap for my company's specific needs?

Answer:
There are a comprehensive array of offerings and materials, classes and workshops offered today that customers can leverage to learn more about the platform and to get very specific skills, whether it is systems programming, DB2®, Linux, or Java™, and we continue to add training based on customer needs. A new class launched late last year — Introduction to z/OS and the IBM System z Environment — was designed for those new to the mainframe environment or involved in a server consolidation, and has been extraordinarily popular with customers. We also continue to add content and courses via the skills portal, accessible via the community Web site above. The most recent addition, based on customer demand, provides customers access to 12 Academic initiative courses, and beginner through advanced curriculum. ibm.com/servers/eserver/zseries/about/charter/skills.html

Question:
What about applications on the platform? Will we have more flexibility and choice?

Answer:
There are over 1,200 partners across the globe providing applications, tools, and solutions for the mainframe in many industries such as banking, insurance and government. Linux in particular has had tremendous growth., with close to 300 ISVs and 850 applications available today. We have invested significantly in WebSphere® on z/OS working with ISVs to port and enable applications. Recent surveys of existing partners indicate the majority of partners are staying current on the latest of OS within 6-9 months of release and also are maintaining or increasing their investment in the mainframe. Partners continue to exploit the mainframe as the data server of choice, and we see more and more partners are beginning to take advantage of the architecture with both data and application on the mainframe as well. You can get more information on available applications from your IBM specialist or Business Partner or by searching the IBM Global Solutions Directory.
Question:
What z9 EC models are available?

Answer:
The following models were announced on July 26, 2005:

- A z9-109 S08 model can be a 1-way through 8-way - which means there are 8 processor units or PUs contained on one book.
- A z9-109 S18 model can be a 1-way through 18-way (18 PUs) contained on two books.
- A z9-109 S28 model can be a 1-way through 28-way (28 PUs) contained on three books.
- A z9-109 S38 model can be a 1-way through 38-way (38 PUs) contained on four books.
- A z9-109 S54 model can be a 1-way through 54-way (54 PUs) contained on four books.

The z9 EC will provide all these same models.

The PUs can be configured as general purpose processors (CPs), Integrated Facilities for Linux (IFLs), System z Application Assist Processors (zAAPs), System z9 Integrated Information Processors (zIIPs), additional System Assist Processors (SAPs), or used as additional spares.

Only eight subcapacity processors can be active on the server (and it doesn’t matter which model you have). When more than eight CPs have been purchased on servers that have more than one book, a selection can be made to activate only 8 or fewer subcapacity features. This means that the new subcapacity settings are available on any of the models as long as they are configured (not the same as purchased) with eight or fewer general purpose processors.

Question:
How does the z9 EC report model number and CPU ID information?

Answer:
Model information is returned via the STSI instruction. The Model Capacity Identifier field in SYSIB 1.1.1 contains model names in the form nxx, where n indicates the subcapacity size (4, 5 or 6, and the original ‘7’ will indicate a full capacity processor), and where xx indicates the number of active general purpose processors (CPs). The Store CPU ID version code, which was used prior to the IBM eServer zSeries 900 (z900) to reflect the model, is always zero for the z9 EC. The Node Descriptor also reports the model, and there are console and z/OS commands which display the node descriptor.
**Question**
What is different about the Model S54?

**Answer:**
The Model S54 is an enhanced capacity model. The z9 EC is fully populated with four books and 54 PUs. You can customize the machine to be a 1 to 54-way. On the standard models, the first book has 8 configurable PUs, with 10 PUs each in books 2, 3 and 4. The S54 has 12 configurable PUs in the first per book, and 14 configurable ones in each remaining book. Like the S38, the S54 can be ordered with a minimum of 16 GB of memory up to a maximum of 512 GB.

**Question:**
How many spare processing units are on the z9 EC?

**Answer:**
There are two spare processing units on the z9 EC. These spares can be shared across the books.

**Question:**
Can any of these spare processing units be used for other purposes?

**Answer:**
No. These spare processing units are only used to provide failover in the remote event of a processor failure: general purpose processors (CPs), Integrated Facilities for Linux (IFLs), System z Application Assist Processors (zAAPs), System z9 Integrated Information Processors (zIIPs) or Internal Coupling Facility (ICFs).

**Question:**
How many System Assist Processors (SAPs) are on the z9 EC?

**Answer:**
The answer depends on the model. The minimum number of SAPs for each model is as follows:

- The z9 EC Model S08 has two.
- The z9 EC Model S18 has four.
- The z9 EC Model S28 has six.
- The z9 EC Model S38 has eight.
- The z9 EC Model S54 has eight.

Additionally, the customer can acquire more SAPs from among the pool of available processing units within the model.

**Question:**
Can I buy a z9 EC that has only IFL or ICF processors without including a general purpose processor (CP)?

**Answer:**
Yes. Similar to the IBM eServer zSeries 990 (z990), you can order only IFLs or ICFs in a z9 EC, using a software model of 700 with 1 to 54 IFLs or a maximum of 16 ICFs.
**Question:**
Will the z9 EC support Token-Ring on the Hardware Management Console (HMC)?

**Answer:**
While the Token-Ring is not offered as a new orderable feature on the z9 EC, current HMCs with Token-Ring may be carried forward to the z9 EC on an upgrade from a IBM eServer zSeries 900 (z900) or z990.

**Question:**
Will the z9 EC support carrying forward the OSA-Express Token-Ring?

**Answer:**
No. OSA-Express Token-Ring is not offered on the z9 EC and cannot be carried forward to the z9 EC on an upgrade from the z900 or z990.

**Question:**
What is the automation enhancement for LPARs?

**Answer:**
Customers can now use automation to change/adjust processor running time via expanded API parameters that have been expanded for task “Change LPAR Controls.”
**z9 EC Upgradeability**

**Question:**
What are the upgrade paths from an IBM eServer zSeries 900 (z900) or a z990 to the z9 EC?

**Answer:**
All models of the z900 (except for the Model 100) and the z990 can upgrade directly to a z9 EC.

**Question:**
What are the upgrade paths from a z9 BC to the z9 EC?

**Answer:**
Any capacity setting on the z9 BC Model S07 can upgrade to the z9 EC Model S08.

**Question:**
Can I upgrade any z9 EC to a Model S54?

**Answer:**
Yes, but it is important to note that upgrading other z9 EC models to the S54 will be disruptive due to the difference in available PUs on the S54.

**Question:**
Can I upgrade from my current full capacity z9-109 to a subcapacity z9 EC?

**Answer:**
Yes. Each of first eight general purpose processors on the z9 EC can be divided into eight sub-units. This creates an 8 by 4 matrix of settings. As long as upgrades are positive MIPS growth, you can move around anywhere within the matrix when adding capacity. When your number of general purpose processors exceeds eight, then all of the general purpose processors must be full capacity.

**Question:**
Can I upgrade to subcapacity processors if I have more than one book?

**Answer:**
Yes. But remember that only eight subcapacity processors can be active on the server (and it doesn’t matter which model you have). When more than eight CPs have been purchased on servers that have more than one book, a selection can be made to activate only 8 or fewer subcapacity features. This means that the subcapacity settings are available on any of the models as long as they are configured (not the same as purchased) with eight or fewer general purpose processors.
**Question:**
What are the migration requirements for customers with Crypto Express2 feature on z890 and z990 servers?

**Answer:**
The Crypto Express2 feature is supported on z990 servers and can be carried forward on an upgrade to the z9 EC.

---

**Question:**
Can I perform a miscellaneous equipment specifications (MES) upgrade involving Crypto Express2 features on the z9 EC without an outage?

**Answer:**
The design of the Crypto Express2 feature allows for nondisruptive upgrades. Unfortunately, there are some environments where a disruptive upgrade will occur: for example; a z9 EC with insufficient I/O slots where an additional I/O cage is required.
Availability Enhancements

**Question:**
What is enhanced book availability and redundant I/O interconnect that are available on the z9 EC?

**Answer:**
Enhanced book availability is an extension of the support for Concurrent Book Add (CBA) delivered on the z990. With proper planning, CBA is designed to allow you to concurrently upgrade a z9 EC by integrating a second, third, or fourth book into the server without affecting application processing.

The z9 EC goes an additional step by allowing a single book in a multibook server to be concurrently removed from the server and reinstalled during an upgrade or repair action. Redundant I/O interconnect provides connectivity to the server I/O resources using a second path from a different book during the action.

To help minimize the impact on current workloads and applications, you should ensure that you have sufficient inactive physical resources on the remaining books to complete a book removal. For maximum availability, it is recommended that z9 EC be purchased with one additional book. Use of enhance book availability may require acquisition of additional hardware resources.

**Question:**
What is the advantage of enhanced driver maintenance on the z9 EC?

**Answer:**
Some of the greatest contributors to downtime during planned outages are Licensed Internal Code (LIC) updates performed in support of new features and functions. When properly configured, the z9 EC is designed to support activating select new LIC levels concurrently. Concurrent activation of the select new LIC level is only supported at specific sync points. Sync points may exist throughout the life of the current LIC level. Once a sync point has passed, you will be required to wait until the next sync point supporting concurrent activation of a new LIC level. Certain LIC updates will not be supported by this function.

Enhanced driver maintenance, exclusive to the System z9, is another step IBM is taking to help reduce the duration of a planned outage.
Question:
What is the advantage of dynamic oscillator switchover?

Answer:
The z9 EC has two oscillator cards, a primary and a backup. In the event of a failure of the primary oscillator card, the backup is designed to detect the failure, switch over, and provide the clock signal to the server transparently. Previously, in the event of a failure of the active oscillator, a server outage would occur, the subsequent Power On Reset would select the backup, and the server would resume operation. Dynamic oscillator switchover is exclusive to the System z9 family.

Question:
What is the flexible memory option on z9 EC?

Answer:
The flexible memory features (FC #2802 - #2824), are offered in 16 GB increments from 32 GB to 384 GB. The features provide resources to support activation of the actual purchased memory increment in the event of a single book failure or to be available during a enhanced book availability action. Flexible memory features are only available on multi-book servers (the S18, S28, S38 and S54 models).

Question:
What is concurrent MBA fanout card hot-plug?

Answer:
A Memory Bus Adapter (MBA) fanout card is designed to provide the path for data between memory and I/O using Self-Timed Interconnect (STI) cables. With the introduction of the z9-109, and now the z9 EC, a hot-pluggable and concurrently upgradeable MBA fanout card became available. Up to 8 MBA fanout cards are available per book for a total of up to 32 MBA fanout cards on the z9 EC when four books are installed. In the event of an outage, an MBA fanout card, used for I/O, may be concurrently repaired using redundant I/O interconnect. By contrast, the MBA cards on the z990, located internal to the Multichip Module (MCM), cannot be hot-plugged and do not support concurrent repair.

Question:
What is the program directed re-IPL feature of the z9 EC?

Answer:
Program directed re-IPL is designed to allow Linux on System z9 to re-IPL without operator intervention. Linux on System z9 is designed to identify how it was IPL'd from the load device. Program directed re-IPL may request (by calling a Licensed Internal Code function) that it be reloaded from the same load device, using the same load parameters. In this way, program directed re-IPL allows Linux on System z9 running natively in an LPAR to trigger a re-IPL. This re-IPL is supported for both SCSI and ECKD™ devices.
Question:
What is the “Small Form Factor Pluggable (SFP)” availability feature of the FICON Express4 adapter?

Answer:
The fiber optic feature on a FICON Express4 may be swapped one at a time with a new like module, should there be a single fiber optic module failure. This swap on a single channel does not affect the traffic flowing on the other channels, and may be performed concurrently, while the other three channels are running.

Question:
What is the improved FICON error recovery feature?

Answer:
The improved FICON error recovery is designed to provide System z9 and z/OS 1.7 I/O recovery processing improvements to allow for the system to detect switch/director fabric problems that may cause FICON links to fail and recover multiple times in a short period of time.

This enhancement is designed to allow the system to detect these conditions and keep an affected path off line until an operator action is taken. This is expected to help limit the performance impacts of switch/director fabric problems. The improved FICON error recovery function is expected to be available in the first half of 2006, and available on z/OS 1.4 and higher with APAR OA13644.
**IFL, zAAP and zIIP specialty engines**

**Question:**
Can I order subcapacity specialty engines?

**Answer:**
No. All specialty engines on the z9 EC run at full capacity.

**Question:**
What is the value of the Integrated Facility for Linux (IFL)?

**Answer:**
The attractively priced IFL processor enables you to purchase additional processing capacity exclusively for Linux workloads, without affecting the MSU rating of the IBM System z model designation. This means that an IFL will not increase charges for System z software running on general purpose processors in the server.

**Question:**
How does Linux benefit from running on the z9 EC?

**Answer:**
The increased power of the z9 EC processor combined with other architectural advances like the increased number of Logical Partitions (LPARs) and the greatly improved internal I/O throughput gives the z9 EC the ability to run increased workloads.

Since the price of the Integrated Facility for Linux (IFL) for this solution package is similar, whether you buy it for the z9 EC, z990, or z900, and the price for Linux software is typically processor-based. This can mean customers may be able to either support larger numbers of users for a similar software price, or provide greater performance for the same number of users at a similar price.

So customers get a price/performance improvement for Linux workload with each successive generation of mainframe technology. Some environments may realize lower and other environments higher improvements.

**Question:**
Is the functionality of the IFL the same as a general purpose processor?

**Answer:**
Yes, the IFL has the same functionality as a general purpose processor on a System z. It supports On/Off Capacity on Demand (On/Off CoD), Capacity Upgrade on Demand for the nondisruptive addition of one or more IFLs, and Capacity BackUp (CBU) for emergency situations. HiperSockets™ can be used for communication between Linux systems, or Linux and other operating systems on the same System z platform.
**Question:**
Can I buy a System z9 server that is made up entirely of IFL processors and no general purpose processors?

**Answer:**
Yes. A dedicated System z9 Linux server is available, meaning that all processors are IFLs on this System z9 server. Please see your IBM or Business Partner representative for more information on it.

---

**Question:**
What is a System z Application Assist Processor (zAAP) and the value to a customer?

**Answer:**
zAAPs enable customers to strategically integrate their Java technology-based Web applications with their core business database environment by providing a more cost-effective, specialized z/OS Java execution environment.

zAAPs can enable customers to run Java Web applications next to mission-critical data for integrated, highly secure and efficient application and database serving. Execution of these new applications within the same z/OS LPAR as their associated database subsystems can help simplify server infrastructures and improve operational efficiencies by reducing the number of TCP/IP programming stacks, firewalls, physical interconnections and their associated processing latencies that might otherwise be required when the application servers and their database servers are deployed on separate physical server platforms.

By executing the Java cycles on a zAAP, you can reduce the demands and capacity requirements on general purpose CPs which may then be available for reallocation to other IBM mainframe workloads.

Furthermore, zAAPs allow customers to purchase additional processing power exclusively for Java workload execution without affecting the total MSU rating or machine model designation, as zAAPs do not carry a rated capacity. Consequently, IBM does not impose software charges on zAAP capacity. Additional IBM software charges will apply only when additional general purpose CP capacity is used.

Moreover, zAAPs may have the effect of reducing charges for subcapacity eligible IBM software products by lowering the rolling 4-hour average MSUs for LPARs with assigned zAAPs.

Best of all, the IBM JVM processing cycles can be executed on the configured zAAPS with no anticipated modifications to the Java application(s).

---

**Question:**
How does Java running on zSeries benefit from the z9 EC?

**Answer:**
The increased power of the z9 EC processor combined with other architectural advances like the increased number of Logical Partitions (LPARs) and the greatly improved internal I/O throughput gives the z9 EC the ability to run increased workloads.
Since the price of the System z Application Assist Processors (zAAPs) for this solution package is similar, whether you buy it for the z9 EC or z990, this can mean customers may provide greater performance for the same number of users at a similar price.

**Question:**
What is the warranty period for System z9 Application Assist Processor (zAAP)? And what is the price of maintenance service for zAAPs?

**Answer:**
During the first 12 months following the installation of a z9 EC machine, zAAPs are under warranty. After the first 12 months the maintenance price for the zAAPs will be similar to the maintenance price for IFLs (Integrated Facilities for Linux).

**Question:**
What is the value proposition of the IBM zIIP?

**Answer:**
IBM zIIPs are designed to help free up general purpose processor (CP) capacity that then may be utilized by other workloads. The zIIP may also help you to leverage the z9 EC, z/OS and DB2 for z/OS qualities of service for data access and information management across your enterprise by helping to make direct access to DB2 more cost effective and potentially reducing the need for many local copies of the data and the complexity that it brings.

**Question:**
What are the hardware and software prerequisites of the IBM zIIP?

**Answer:**
When you plan to add a zIIP to an existing z9-109 server (shipped prior to May 26, 2006), the server requires a driver level that will be available on May 26, 2006. If you purchase a new z9 EC with a zIIP that ships on or after May 26, 2006, the appropriate driver level will ship with the new server. Operating system support, which enables the redirecting of eligible workloads to the zIIP, will be provided by z/OS 1.6 or 1.7 via a Web download. The zIIP will be supported by DB2 UDB for z/OS V8 with enabling PTFs. All enabling PTFs will be available June 30, 2006.

**Question:**
How will IBM zIIPs work?

**Answer:**
The IBM zIIP’s execution environment will accept eligible work from z/OS 1.6 or 1.7, which will manage and direct the work between the general purpose processor (CP) and the zIIP. DB2 UDB for z/OS V8 will exploit the zIIP capability for eligible workloads.
**Question:**
Tell me more about how the IBM zIIPs will work.

**Answer:**
The IBM zIIP is designed so that a program can work with z/OS 1.6 or 1.7 to have all or a portion of its enclave Service Request Block (SRB) dispatched work directed to the zIIP. The z/OS operating system, acting on the direction of the program running in enclave SRB mode, controls the distribution of the work between the general purpose processor and the zIIP. Using a zIIP can help free up capacity on the general purpose processor. When the zIIP is available, DB2 UDB for z/OS V8 will exploit the zIIP by sending eligible work to z/OS 1.6 or 1.7 that can be directed to the zIIP.

The types of eligible DB2 UDB for z/OS V8 workloads executing in SRB mode, which all or a portion of can be sent to the zIIP, are:

1. Network Connected Application Types – An application (running on UNIX®, Linux, Intel®, Linux on System z9 (or z/OS) may access a DB2 UDB for z/OS V8 database that is hosted on a z9 EC. Eligible work that can be directed to the zIIP are portions of those requests made from the application server, to the host, through SQL calls over a DRDA® via TCP/IP connection. Examples of workloads that may be running on the server connected over DRDA via TCP/IP to the z9 EC may include BI, ERP or CRM application serving.

2. Data Warehousing Application Types – Requests that utilize DB2 UDB for z/OS V8 for long running queries, including complex star schema parallel queries, may have portions of these SQL requests directed to the zIIP when DB2 gives z/OS the necessary information. These queries are typical in data warehousing implementations. The addition of select long running parallel queries may provide more opportunities for DB2 customers to optimize their environment for data warehousing while leveraging the unique qualities of service provided by System z9 and DB2.

3. Utility Function Types – A portion of DB2 utility functions that are used to maintain index maintenance structures (LOAD, REORG and REBUILD INDEX) can be redirected to the zIIP.

**Question:**
Once a zIIP is installed, how is workload on it measured?

**Answer:**
Once a zIIP is installed (along with z/OS or z/OS.e 1.6 or 1.7 (with PTF) and DB2 UDB for z/OS, V8 (with PTF), monitoring the zIIP activity will be similar to monitoring zAAP activity. Specifically:

- Set up WLM policy with Service Class(es) for SUBSYSTEM TYPE=DDF
- **RMF**™ Monitor 1 Type 70 Record will monitor overall zIIP activity:
  - Logical processor busy as seen by z/OS is reported
  - Physical processor busy as seen by LPAR is reported
- **RMF** Monitor 1 Type 72 Record will show more detail:
  - The amount of time spent executing on zIIP processors is reported
  - Usage and Delay sample counts for zIIP eligible work is reported
In addition, DB2 accounting trace records can provide information on the zIIP. IBM Tivoli®
OMEGAMON® XE for DB2 Performance Expert on z/OS, DB2 Performance Expert or IBM
Tivoli OMEGAMON XE for DB2 Performance Monitor on z/OS can be used to monitor the
zIIP information.

Question:
Why was DRDA® processing chosen for zIIP eligible workload?

Answer:
Database workloads such as CICS®, IMS™, stored procedures and batch have become
increasingly efficient and cost effective for the mainframe. Today, customers are looking to
further leverage their data on the mainframe and are turning to the mainframe more and more
frequently for new application workloads. These application workloads, such as Enterprise
Resource Planning (ERP), Customer Relationship Management (CRM) and Business
Intelligence (BI) often use DB2 as a database server.

Added system costs for network access to DB2 results in hardware and software charges that are
substantially higher for remote applications, when compared to the same workload running as a
local batch application. The zIIP is aimed at helping customers more cost effectively run these
database serving applications on the mainframe. Our objective with the zIIP is to help bring the
costs of network access to DB2 more closely in line with the costs of running similar workloads
under CICS, IMS or batch on the mainframe.

Question:
If my application is a business intelligence application that is also network attached (using
DRDA over TCP/IP), does this mean that my workload can leverage the eligible redirect for star
schema parallel processing and DRDA over TCP/IP?

Answer:
Yes, you can benefit from both eligible redirects. If a star schema parallel query comes in
remotely via DRDA over TCP/IP, a portion of the workload coming across the DRDA over
TCP/IP connection can be redirected to the IBM zIIP, as well as the portion of the star schema
parallel query processing that is redirected.

Question:
Can my application on z/OS take advantage of both zAAPs and zIIPs?

Answer:
The use of zAAPs and zIIPs by a single transaction flow is not mutually exclusive. The two
specialty engines are designed to run two different types of work. The zAAP processor provides
a specialized processor for running Java applications and the zIIP is designed to redirect
workload that is typically more closely associated with operating system processing. When
available, it will be possible for transactions and/or batch to use a zIIP to handle eligible portions
of the DRDA over TCP/IP processing for DB2 UDB for z/OS V8, while also running Java stored
procedures on a zAAP on the same z9 EC.
**Question:**
If Linux on System z9 is running in an LPAR on the z9 EC, accessing DB2 UDB for z/OS V8, can portions of the eligible work be redirected to the zIIP?

**Answer:**
Yes. Whether the customer is using HiperSockets or some other network to access the DB2 database, the Linux application can take advantage of redirection of processing to the zIIP.

**Question:**
What can be the advantage of having the management of specialty engines in separate pools on z9 EC?

**Answer:**
On previous systems all ICFs, IFLs, and zAAPs were managed as a single type of physical resource on the server (as if they were all ICFs). This complicated capacity planning on machines that have more than one type of these processors installed and treated them as shared processors. It was difficult for customers to keep the shared logical resources within the bounds of the proper physical resources since they were all managed as this single pool of shared resources. And, they all showed up in RMF as ICFs. By separating the management of these resources on the System z9 with this new support, all logical resources (ICFs, IFLs, zAAPs and now the zIIPs) will be managed separately within their corresponding physical resource pool. RMF will report all four types of processors (including general purpose processors which have not changed here) at both the logical partition level and the physical resource level. Note that in previous machines, you were able to specify processing weights for shared logical ICFs in coupling facility partitions and/or shared IFLs in Linux mode partitions but these weights and management would all be grouped together in the physical "ICF" resource pool. By separating the "ICFs" into their separate pools and managing them independently, customers can accurately make capacity planning decisions on the correct processor type needs according to how the processors are truly being utilized.

**Question:**
What will be the advantage of the improved LPAR weight management of CPs, zAAPs and now the zIIP on a System z9?

**Answer:**
On previous systems, the introduction of zAAP created the first Logical Partition (LPAR) type that could have multiple types of shared logical processors; namely shared general purposed processors and shared zAAPs. What was not available was the ability to set a different processing weight value for the zAAPs in an LPAR. Whatever weight was set for the shared general purpose processor was effectively replicated for the shared zAAPs in the LPAR. The System z9 support allows a separate specification of a processing weight for the zAAPs, and now zIIPs in an LPAR, thus allowing flexible changes to the independent weight values within their respective processing pools.
**Question:**
Will the z/OS support for WLM goal mode management for zAAP processors be extended to the zIIP specialty engines?

**Answer:**
Yes. WLM will monitor work priorities on zIIPs and zAAPs so that goals can be met.

**Question:**
Where can I get more information on the zIIP?

**Answer:**
Clarification on a few DB2 terms for better zIIP understanding

Question:
What are enclave SRBs? And how are they related in the zIIP discussion?

Answer:
z/OS dispatches work in either TCB (Task Control Block) or SRB (Service Request Block) mode. DB2 parallel tasks use SRB mode and are assigned the same importance as the originating address space.

Preemptable enclaves are used to do the work on behalf of the originating TCB or SRB address space. Enclaves are grouped by common characteristics and service requests and since they are preemptible, the z/OS dispatcher (and WLM) can interrupt these tasks for more important ones (i.e. Manage a transaction end-to-end). There are two types of preemptible SRBs: client SRBs and enclave SRBs.

If a DB2 UDB for z/OS V8 request is coming in over distributed connection (i.e. DRDA over TCP/IP) then most of their work is executed in enclave SRBs.

If the request is coming over local / native connection, then that work is dispatched between TCBs, client SRBs, and enclave SRBs (star schema parallel queries and some utility index maintenance now use enclave SRBs).

Only the enclave SRB work (not the client SRB work or non-preemptible SRB or TCB work) is eligible to be redirected to the zIIP. DB2 UDB for z/OS V8 knows how its work is dispatched and directs z/OS 1.6 or later to dispatch (redirect) a portion of the eligible work to the zIIP.

Question:
What is a star schema parallel query?

Answer:
The star schema is the simplest data warehousing data model. It consists of a single “fact table” at the center and the “dimension tables” at the points, hence the star term. A fact table typically contains metrics/numeric data about the performance of a business process by a business entity over a period of time. Sales History is a good example of this. The who, what, where, and when associated with the business measurements are typically encoded data columns (dimensions) in the fact table. Each dimension table contains a set of related codes and additional descriptive data columns about each code. For example a sales office dimension table could include office code, office name, street address, city, state, zip and country. A product dimension table could include product name, price, size, weight and manufacturer.

To create a meaningful management sales report, one could present performance data from the fact table and translate coded data from the dimension tables in the result rather than just present coded data from the fact table. A single (joined) query is typically submitted to DB2 to retrieve the data. Fact tables can have millions, even billions of rows, and queries may need to process a sizable portion of the table. To reduce query elapsed time, DB2 will assess whether the single
query should be executed in parallel by multiple threads with minimal contention, and if possible, will split the single query to execute concurrently on multiple processors.

A star schema parallel query is a single query (SELECT statement) that runs against tables with a star schema relationship that DB2 chooses to execute in parallel on multiple processors. The zIIP could be utilized as one of the processors.
### z9 EC Pricing

**Question:**
Is hardware pricing for z9 EC similar to pricing on z990?

**Answer:**
Yes, hardware pricing for z9 EC is very similar to z990 in approach. As in the past, each customer order will carry a unique price driven by the customer’s specific configuration needs and desires.

**Question:**
What is the memory pricing on the z9 EC?

**Answer:**
System z9 EC memory will be priced as low as $8K per GB.* (* This is the USA price, and prices will vary by country.)

**Question:**
How will diagonal upgrades from z990 (or z900) to z9 EC be priced?

**Answer:**
The platform’s value proposition of net pricing diagonal upgrades (upgrades from z900 or z990) will not change. As in the past, microprocessor and memory feature conversions will be the key net priced items.

**Question:**
How will maintenance be priced on z9 EC?

**Answer:**
Monthly 24x7 ESA list prices will exist and applied against the z9 EC feature codes similar to z990.

**Question:**
What is the cost of the IFL, the zAAP and the zIIP?

**Answer:**
The price of the IFL, the zAAP and the zIIP will be US $125,000 in the United States. Pricing may vary in other countries.

**Question:**
Is software pricing on z9 EC similar to software pricing on z990?

**Answer:**
The approach for pricing software on the z9 EC is similar to pricing on the z990, but, in line with the pricing initiatives announced with the Mainframe Charter in August 2003. The announced MSUs for software pricing purposes on the z9 EC are on average 10% less than those for z990. With this, in many cases, software savings can be realized on the z9 EC versus the z990.
**Question:**
Is software pricing on z9 EC similar to software pricing on z900?

**Answer:**
The approach for pricing software on the z9 EC is similar to pricing on the z900, but, in line with the pricing initiatives announced with the Mainframe Charter in August 2003. The announced MSUs for software pricing purposes on the z9 EC are on average 19 less than those for z900. (The z990 offered 10% savings in software pricing MSUs when coming from a z900 and the z9 EC has an additional 10% savings for a total average of 19% over the z900). With this, in many cases, software savings can be realized on the z9 EC versus the z900.

**Question:**
How does a customer pay for software on the z9 EC server?

**Answer:**
Customers may pay for Monthly License Charge software under either Workload License Charges (WLC) or Parallel Sysplex License Charges (PSLC). Full-Capacity WLC is available on z9 EC with the z/OS operating system. Subcapacity WLC is available on a z9 EC that has the z/OS operating system and has discontinued both use and licensing of the OS/390® operating system, subject to applicable terms and conditions. Once qualified, customers may determine when they wish to adopt WLC. PSLC is available on z9 EC in all environments, including a standalone (non-coupled) z9 EC, subject to applicable terms and conditions.

Customers using z/VSE V3.1 and VSE/ESA products running on a z9 EC server will qualify for the same announced pricing terms available on z990. Extended License Charge (ELC) applies for servers over 80 MSUs. z/VM V4.4, and later, and its support charges are priced based on the number of processors according to International Program License Agreement (IPLA) and Subscription and Support (S&S) terms and conditions.

**Question:**
What is a "software model"? Is this something new for z9 EC?

**Answer:**
No, software models are not new for z9 EC. Like the previous IBM z990 mainframe servers, the machine type and model of the z9 EC does not indicate the machine's capacity. Instead, you must know the number of installed general purpose processors (CPs) to determine the machine's Model-Capacity Identifier for software licensing/charging purposes. The software model follows the nomenclature nxx, where the n indicates the subcapacity size (4, 5, or 6, and the original ‘7’ will indicate a full capacity processor), and where xx indicates the number of installed general purpose processors. You may research MSU ratings per software model on the Web at [ibm.com/zseries/library/swpriceinfo](http://ibm.com/zseries/library/swpriceinfo).
**Question:**
Are there purchase restrictions limiting the number of zIIPs I can order?

**Answer:**
A customer may order zIIPs up to the number of permanently purchased general purpose processors (CPs) on a given z9 EC model. This requirement is at a server level, so a customer could have an LPAR with more zIIPs than general purpose processors (CPs), as long as there are enough general purpose processors (CPs) in the entire server to meet the one for one requirement.

**Question:**
If I currently have one general purpose processor (CP) and one zAAP, can I order one zIIP without needing to order another general purpose processor (CP) to meet the restriction listed above?

**Answer:**
Yes. One zAAP and one zIIP can share one general purpose processor (CP) to satisfy the ordering restrictions.

**Question:**
What impact could the IBM zIIP have on IBM software charges?

**Answer:**
IBM does not impose software charges on zIIP capacity. Additional IBM software charges will apply when general purpose processor (CP) capacity is used. The amount of general purpose processor savings will vary based on the amount of workload executed by the zIIP, among other factors.

**Question:**
What impact could the IBM zIIP have on non IBM software charges?

**Answer:**
Customers are encouraged to contact their specific ISVs/USVs directly to determine if or how their charges will be affected.
Question: What is the basis of the Large System Performance Ratios (LSPR)?

Answer: The LSPR ratios reflect the range of performance between prior zSeries servers and the z9 EC as measured using a wide variety of application benchmarks. The latest release of LSPR contains a number of updates to reflect the continuing evolution of zSeries customer applications and configurations. First, the workload suite has changed: the short running batch workload (CB-S) is replaced with a Java based batch workload (CB-J). Second, all workloads were moved to more recent levels of subsystem and compiler software. Third, and most significant, the measurement environment for the LSPR now includes both single-image z/OS and multi-image z/OS in separate tables. For details on the workloads and performance ratios, please reference ibm.com/servers/eserver/zSeries/lspr.

Question: Why are there two tables in the latest LSPR?

Answer: The LSPR has been enhanced to include performance ratios reflecting both “single-image” z/OS and “multi-image” z/OS environments. Traditionally, the data presented in the LSPR was based on processors configured with one z/OS image equal in size to the processor model (the exception to this was that for z990 models with greater than 16 CPs, two images of z/OS were used). Typically, zSeries processors are configured with multiple images of z/OS. Thus, the LSPR now includes a table of performance ratios based on average multi-image z/OS configurations for each processor model as determined from the profiling data. Since the multi-image z/OS table is much more representative of the vast majority of customer configurations, it is used as the basis for setting MIPS and MSUs for the z9 EC.

Question: What multi-image configurations are used to produce the LSPR multi-image table?

Answer: A wide variety of multi-image configurations exist. The main variables in a configuration typically are: 1) number of images, 2) size of each image (number of logical engines), 3) relative weight of each image, and 4) overall ratio of logical engines to physical engines. The configurations used for the LSPR multi-image table are based on the average values for these variables as observed across a processor family. It was found that the average number of images ranged from 5 at low-end models to 9 at the high end. Most systems were configured with 2 major images (those defined with >10% relative weight). On low- to midrange models, at least one of the major images tended to be configured with a number of logical engines close to the number of physical engines. On high-end boxes, the major images were generally configured with a number of logical engines well below the count of physical engines reflecting the more common use of these processors for consolidation. The overall ratio of logical to physical engines (often referred to as “the level of over-committment” in a virtualized environment)
averaged as high as 5:1 on the smallest models, hovered around 2:1 across the majority of models, and dropped to 1.5:1 on the largest models.

**Question:**
Which LSPR table should I use for capacity sizing?

**Answer:**
For high-level sizing, most users will find the multi-image table to reflect configurations closest to their own. This is simply due to the fact that most systems are run with multiple z/OS images. However, the most accurate sizing requires the zPCR tool, which can be customized to match a specific multi-image configuration rather than the average configurations reflected in the multi-image LSPR table.

**Question:**
If I compare the two tables, why are the capacity ratios for some models higher in the single-image table while other models have higher ratios in the multi-image table?

**Answer:**
Just as capacity ratios are sensitive to workload characteristics (note the varying capacity ratios within a table associated with different workloads), capacity ratios will also be sensitive to the configuration of z/OS images on a processor. If one compares a processor configured only with a single, large z/OS image to the same processor configured with multiple z/OS images, there are both pluses and minuses that come into play. There is a cost incurred to manage multiple z/OS images and their associated logical processors. There is also a cost incurred as the size of a z/OS image increases. Thus, if one compares a configuration of a single large z/OS image to a configuration of multiple but smaller z/OS images, the net result can vary as the magnitude of the pluses and minuses will vary. The sensitivity of the multi-image configurations to the number of images, size of each image, relative weights and overall logical: physical ratio will cause a fair amount of variability in the capacity ratios of these configurations. The multi-image table provides a representative view of these ratios based on average configurations. However, “your mileage will vary” applies here as configurations deviate from the average. The zPCR tool can provide capacity ratios customized to specific configurations.

**Question:**
How much variability in performance should I expect when moving a workload to a z9 EC?

**Answer:**
As with the introduction of any new server, workloads with differing characteristics will see some variation in performance when moved to the z9 EC. The performance ratings for a server (MSUs) are determined by averaging the performance of a variety of workloads that represent what we understand to be the major components of our customers’ production environments. While the ratings provide good “middle-of-the-road” values, they do represent an average, and by definition some workloads fall higher than the average and some workloads fall below. The z9 EC has been specifically designed to focus on new and emerging workloads while continuing to provide outstanding benefit to the predominant traditional applications. The superscalar design takes advantage of naturally occurring code sequences designed to provide all workloads with a significant boost in performance. Additionally, newer applications, such as those with
compiler optimizations for the z9 EC (available with z/OS 1.7 and Java 5) may see even higher benefits, particularly those that may be enhanced over time to exploit some of the new instructions provided with the z9 EC.

**Question:**
Once my workload is up and running on a z9 EC, how much variability in performance will I see?

**Answer:**
Minute-to-minute, hour-to-hour and day-to-day performance variability generally grows with the size (capacity) of the server and the complexity of the LPAR configuration. With the goal for the z9 EC of introducing a design that can provide the capability to deliver nearly 2 times the capacity of the largest previous server and to support a much higher number of logical partitions, significant enhancements to the LPAR management algorithms have been made to help reduce the potential for increased performance variability. In the spirit of autonomic computing, the LPAR manager is designed to automatically place and dispatch logical partitions to help optimize the performance of the hardware, and minimize the interference of one partition to another. However, while the average performance of workloads is expected to remain reasonably consistent when viewed at small increments of time or by individual jobs or transactions, performance could potentially see more variation than in the past simply due to the expected larger and more complex LPAR configurations that can be supported by the z9 EC.

**Question:**
What is the performance improvement a VSE customer might experience on the z9 EC?

**Answer:**
The performance ratios that a VSE customer workload might experience when migrating from a zSeries server to the z9 EC is represented by the range of ratios seen by a comparable z/OS migration. For example, the published ratio in the LSPR between the z990 2084-301 and the z9 EC 2094-701 is approximately 32% to 39%. VSE workloads could expect this same range of performance.

**Question:**
How do I get performance information for my TPF products running on a System z9?

**Answer:**
For the TPF V4 and z/TPF V1.1 products, the TPF Workload Specifics ITRRs should be used. For more information please contact your TPF Support Representative, or send a request to tpfqa@us.ibm.com.
Question:
What capabilities are extended with the z9 EC to enhance performance for z/VM Linux guests?

Answer:
The z990 and z890 servers introduced technology designed to enhance performance by allowing adapter interrupts to be passed directly to z/VM operating system guests for HiperSockets, Fibre Channel Protocol (FCP), and OSA on z990 and z890 servers. The System z9 introduced a complementary technology, which is also available for z990 and z890.

- QDIO Enhanced Buffer-State Management (QEBSM) designed to help eliminate the overhead of hypervisor interception.
- Host Page-Management Assist (HPMA) – designed to allow the hardware to assign, lock, and unlock page frames without z/VM hypervisor assistance.

These hardware assists can allow a cooperating guest operating system to initiate QDIO operations directly to the applicable channel, without interception by z/VM, thereby helping to provide additional performance improvements. These virtualization technologies are available only to first-level guests of z/VM V5.2. IBM is working with its Linux distribution partners that this function will be provided in future Linux on System z9 distribution releases or service updates.

(Minimum MCL level is required on the z990 and z890. Refer to the Preventive Planning (PSP) bucket for your z990 and z890 server for required updates.)

Question:
Can HiperSockets support Internet Protocol Version 6 (IPv6)?

Answer:
Yes. HiperSockets now supports Internet Protocol Version 6 (IPv6) designed by the Internet Engineering Task Force (IETF) to replace Internet Protocol Version 4 (IPv4) to help satisfy the demand for additional IP addresses. IPv6 provides more unique IP addresses by expanding the IP address space from 32 bits to 128 bits. IPv6 is supported by z/OS 1.7 and z/VM 5.2.

Question:
What capability is offered on the z9 EC to ease migration for customers using traditional SNA and the IBM 374X?

Answer:
The OSA for NCP feature provides LPAR to LPAR connectivity from System z9 operating systems to the Communication Controller for Linux (CCL) V1.1. Use of the OSA for NCP can help ease migration and eliminate dependencies on hardware such as the 3745/6 communication controller. It will help protect your investment in traditional SNA applications and data, while allowing you to collapse SNA within the server while exploiting and leveraging IP.
**Question:**
What is on the z9 EC to help to simplify networking administration and management of VLANs on the z9 EC?

**Answer:**
The OSA-Express2 features now support GVRP for VLAN prioritization (a component of the IEEE 802.1 standard) to help simplify networking administration and management of VLANs. Generic Attribute Registration Protocol (GARP) VLAN Registration Protocol (GVRP) can be used to propagate VLAN information with switches. You may no longer be required to manually enter VLAN IDs at the switch. Support of GVRP is exclusive to System z9, is applicable to all of the OSA-Express2 features when in QDIO mode, and is supported by z/OS. z/VM 5.2 will provide this support on May 26, 2006 with the PTF for APAR VM63952 and z/VM V5.1 with the PTFs for APAR VM63784 and PK08444.

**Question:**
What was introduced on the z9 EC with the OSA-Express2 support for 1000BASE-T Ethernet?

**Answer:**
With the additional memory available on OSA-Express2, the 1000BASE-T Ethernet feature will be able to: Support large send (offloading TCP segmentation processing from host TCP/IP stack to OSA-Express2), 640 TCP/IP stacks for improved virtualization by hosting more images on System z9 and potentially reducing the number of required OSA features, and concurrent LIC update to help minimize network traffic disruption.
Question:
What is the FICON Express4?

Answer:
FICON Express4 is a new generation of FICON/FCP features now being offered on the IBM System z9, designed to deliver increased performance compared to the FICON Express2 on the z990 and z890. FICON Express4 now offers two unrepeated distance options (4 KM and 10 KM) when using single mode fiber optic cabling, and also supports a 4 Gbps link data rate with auto-negotiation to 1 and 2 Gbps for synergy with existing switches, directors and storage devices.

Question:
What are the key benefits of FICON Express4 support for 4 Gbps links?

Answer:
FICON Express4 can help to reduce the cost of storage operations by enabling consolidation with improved throughput and faster link data rates and help to reduce configuration complexity. FICON Express4 can also enable a more manageable migration to higher performance by supporting devices at 1, 2 or 4 Gbps. Customers may deploy 4 Gbps in the SAN backbone or to selected devices, while preserving investment in 1 or 2 Gbps connected devices.

In addition, FICON Express4 can enable a more cost effective exploitation for midrange customers with a 2 port LX FICON Express4 feature available exclusively on the z9 BC.

Question:
Are FICON Express4 features supported on zSeries or S/390 processors?

Answer:
No. FICON Express4 support is exclusive to System z9. However, because it supports a 4 Gbps link data rate with auto-negotiation to 1 and 2 Gbps, zSeries or System/390™ processors operating at 1 or 2 Gbps as well as other, slower speed FICON and FCP devices are compatible with 4 Gbps capable SAN Directors, and Switches.

Question:
What new IBM System Storage products will attach to FICON and FCP 4 Gbps enabled SAN?

Answer:
On April 27, 2006 IBM also announced an expanded range for 4 Gbps FICON and FCP enabled SAN directors and switches for enterprise and midrange environments. Announcements included:

- Cisco MDS 9513 director and 4 Gbps features for 9216A and 9216i Switches; 9506 and 9509 directors
- IBM TotalStorage® SAN32M-2 switch and SAN140M director (McDATA) 4 Gbps features. (IBM SAN256M 4 Gbps feature statement of direction, January 2006)
- IBM TotalStorage SAN32B-2 switch and SAN 256B director (Brocade) 4 Gbps shortwave features planned to be expanded with 4 Gbps long wave features in 2Q/2006.

IBM System Storage is ready to support System z9 and FICON Express4 with a large variety of storage options that are compatible with the new 4Gbs FICON/FCP adapters. We have disk, tape and SAN products that either support 4Gb/s connectivity today or will very soon. These links are all auto-negotiating so they can support intermix of 4Gbs and 2Gbs technology.

Question:
What has IBM announced for tape encryption and how will it benefit me?

Answer:
On August 29, 2006, IBM was first to announce tape encryption using its TS1120 tape drive. When invoked, the new feature will encrypt data as it is written to the tape media. The new feature will especially help customers protect tape cartridges that are being moved from one location to another.

Question:
I’ve already purchased a TS1120 tape drive; will I be able to upgrade it to support the new encryption feature?

Answer:
Yes, there is a plan to provide an upgrade for customers that previously purchased a TS1120 tape drive prior to the availability of the encryption feature.

Question:
How are the Encryption keys going to be generated and managed?

Answer:
When used with z/OS, the TS1120 leverages the security of System z and cryptographic features to provide a powerful solution that can be enabled for enterprise wide encryption key storage and management using ICSF.

Question:
How does FICON Express4 help business continuity and GDPS® configurations?

Answer:
FICON Express4 can help improve business continuity in two ways. IBM already enjoys some of the lowest latency, lowest TCO, an most resilient design points for high availability configurations. With FICON Express4 configurations will have the ability to scale larger with better throughput, provide a lower TCO, and continue to offer a proven solution.

In addition, FICON Express4 continues support for FICON cascading directors. Support for FICON cascading means that a Native FICON (FC) channel or a FICON CTC can connect a server to a device or other server via two (same vendor) directors in between. FICON Cascading
support is important because it can help provide high availability by reducing configuration complexity and by reducing the number of channels for interconnecting the 2 sites.

Question: How does the z9 EC deliver increased I/O connectivity and simplify I/O configuration?

Answer: In comparison to the z990, the z9 EC supports a 40% increase in Crypto, FICON and OSA connectivity per I/O cage by providing the capability to populate the 28 I/O slots in one I/O cage with any mix of up to 8 Crypto Express2, up to 28 FICON Express2 or FICON Express, and up to 24 OSA-Express2 or OSA-Express features. On the z990, the maximum quantity of Crypto, FICON, and OSA features in combination in one I/O cage was limited to 20 features because of power/mechanical limitations. Now up to 336 FICON channels may be installed on the z9 EC. Up to 240 FICON channels may be installed on the z990.

Question: How can Modified Indirect Data Address Word Facility (MIDAW) help improve application performance?

Answer: MIDAW is a facility that can help improve FICON performance by reducing channel, director, and control unit overhead. Applications that use the following datasets to access information may benefit, including DB2, VSAM, Partitioned Data Set Extended (PDSE), Hierarchical File System (HFS), z/OS File System (zFS), and other extended format datasets.

Question: What are the performance improvements found running tests on the z9 EC, MIDAW facility, FICON Express4, DB2 UDB for z/OS V8 and an IBM TotalStorage® DS8000?

Answer: The results of internal DB2 table scan tests with Extended Format datasets on the z9 EC with the Modified Indirect Data Address Word (MIDAW) facility and the IBM TotalStorage DS8000 yielded the following results when using FICON Express4 operating at 4 Gbps on a z9 EC compared to FICON Express2 operating at 2 Gbps on a z9-109:

- A 46% improvement in throughput for all reads (270 MBps vs 185 MBps)
- A 35% reduction in response times

In addition, comparing use of the MIDAW facility with FICON Express4 operating at 4 Gbps to the use of MIDAWs with FICON Express2 operating at 2 Gbps showed a combined throughput improvement of greater than 220% for all reads (270 MBps vs 84 MBps) on DB2 table scan tests with Extended Format datasets.

These measurements are examples of what has been achieved in a laboratory environment using one FICON Express4 channel operating at 4 Gbps (CHPID type FC) on a z9 EC with z/OS 1.7 and DB2 UDB for z/OS V8. Performance is based on measurements and projections using standard IBM benchmarks in a controlled environment. The actual throughput or performance that any user will experience will vary depending upon considerations such as the amount of multiprogramming in the user’s job stream, the I/O configuration, the storage configuration, and
the workload processed. Therefore, no assurance can be given that an individual user will
achieve throughput or performance improvements equivalent to the numbers stated here.

Question:
What enhancements on the z9 EC help to provide increased I/O addressing capability?

Answer:
The z9 EC helps to increase the number I/O devices and amount of addressable storage with the
following enhancements:

- Multiple Subchannel Sets: MSS provides a second set of subchannels for defining Parallel
  Access Volume (PAV) aliases. This function can help provide relief from the 64K device
  limit by allowing PAV aliases to be defined without making device numbers unavailable for
  defining additional devices. For some of our largest customers this is designed to provide
  greater I/O device configuration capabilities.

- 63.75K subchannels: The z9 EC addresses a maximum of 64K-1 subchannels in subchannel
  set 0 (zero). Previously 1024 (1K) of these subchannels were reserved for system use. IBM
  is making 768 of these subchannels available for customer use. This increases the storage
  attachment capability of the z9 EC – for example the IBM TotalStorage DS8000 Series can
  be defined to attach 63.75K unit addresses – so with 63.75K in the host there is symmetry
  between the server and the storage subsystem.

Question:
What is N_Port ID Virtualization announced with the System z9?

Answer:
N_Port ID Virtualization (NPIV) allows a single Fiber Channel Protocol (FCP) port on the
System z9 platform to appear as multiple, distinct ports providing separate port identification and
security zoning within the fabric for each operating system image as if each operating system
image, or z/VM guest, had its own unique physical port. NPIV is similar to the Multiple Image
Facility (MIF) available on ESCON® channels.

NPIV is an extension of the International Committee for Information Technology
Standardization (INCITS) Fibre Channel standard. NPIV applies to the FICON features
supported on z9 EC when a channel is configured as CHIPID type FCP, supporting connectivity
to SCSI devices.
Question:
How does On/Off CoD work?

Answer:
Yes. On/Off CoD is available for up to twice the ‘purchased’ capacity for a given machine based on the LSPR mixed workload multi-image ITRR (Internal Throughput Rate Ratio). Please note that while you can not decrease the number of installed general purpose processors, with the full ‘matrix’ upgradeability of the subcapacity processors you can temporarily change capacity with processors of equal, less or greater capacity depending on your requirements.

Note that On/Off CoD for specialty engines will always be full capacity processors.

Question:
What is the administrative On/Off Capacity on Demand (On/Off CoD) test?

Answer:
This feature allows the customer to order ‘zero’ quantity of processor features via Resource Link™, which will allow customer staff to order/test/rehearse/train/document the entire On/Off CoD process without incurring any cost. There is an unlimited number of such tests allowed and there are no time period restrictions.

Question:
What is the change available that allows adding more On/Off CoD capacity without having to restore the System z9 to the current ‘purchased’ capacity?

Answer:
With an On/Off CoD temporary upgrade already installed, you can now modify (add or remove) your ‘temporary’ capacity configuration without having to first restore the machine to its original “permanent” capacity configuration. You are still restricted to an upper bound for temporary upgrade size of no more than double the machine’s purchased capacity configuration. You will be charged for the additional capacity on a 24 hour basis, and if you increase capacity multiple times during a 24 hour period, the charge will apply to the greatest amount of capacity activated.

Question:
What is the API that is available for On/Off CoD?

Answer:
There will be a API provided within the existing HMC SNMP command which is designed to enable customers to use other automation code (which conforms to the API) to enable activation of On/Off CoD on the HMC without human intervention. This will allow for flexibility of operation of the On/Off CoD function.
**Question:**
What is unassigned capacity?

**Answer:**
Unassigned capacity can be either general purpose or IFL capacity that has been purchased by the end user, but for business reasons has been unassigned (turned off) by the end user so no business processing can be executed on the unassigned capacity.

**Question:**
What is the hardware price of On/Off CoD when using unassigned capacity compared to capacity that has not been previously purchased?

**Answer:**
On/Off CoD pricing has been enhanced to provide reduced pricing when a temporary upgrade includes activation of previously purchased but unassigned general purpose processor (CP) capacity and/or previously purchased but unassigned IFLs. Now you may activate any amount of purchased but unassigned CP capacity or purchased but unassigned IFLs on a temporary basis for a flat daily fee per processor type. There is one flat daily fee for temporary usage of purchased but unassigned CP capacity and a second, separate flat daily fee for temporary usage of purchased but unassigned IFLs. The offering is designed such that when you activate purchased but unassigned capacity you'll be charged the lesser of the flat daily fee or your standard On/Off CoD rate. Contact your IBM System z sales specialist for additional pricing information.

**Question:**
Can On/Off CoD utilize unassigned general purpose processor capacity as a temporary zAAP or zIIP on the z9 EC?

**Answer:**
Not always. If the unassigned CP capacity is a subcapacity processor, no. If the unassigned capacity is a full capacity processor, yes. Specialty engines require full capacity processors. So only an unassigned full capacity CP may be utilized as a specialty engine for On/Off CoD.

**Question:**
Can On/Off CoD use unassigned IFL or CP capacity for any purpose other than its original intended purpose?

**Answer:**
Yes. The processor reserved as the unassigned IFL capacity can be activated as any other On/Off CoD processor type.
Question:
Does the customer need to follow a different process to utilize unassigned capacity with On/Off CoD?

Answer:
No. The order process is the same. Resource Link will give the customer a priced option.

Question:
Will temporary capacity be available for specialty PUs (IFLs, ICFs, zAAPs and zIIPs) on the z9 EC?

Answer:
Yes. The z9 EC, like the z890 and z990 servers, will continue to have the ability to utilize capacity for temporary upgrades or special purpose processors through On/Off CoD: IFLs, zAAPs, zIIPs and ICFs. Please note that SAPs are not available for On/Off CoD upgrades.

Question:
Can I order On/Off CoD if I have subcapacity processors?

Answer:
Yes. Again, On/Off CoD is available for up to twice the ‘purchased’ capacity for a given machine and you can not decrease the number of installed general purpose processors. With the full ‘matrix’ upgradeability of the subcapacity processors you can temporarily change capacity with processors of equal, less or greater capacity depending on your requirements. When you use subcapacity settings for your temporary On/Off CoD capacity, you can not exceed 8 general purpose processors. (NOTE – you could have subcapacity general purpose processors and, when you execute On/Off CoD, as long as you don’t exceed more than twice your purchased capacity, have 8 or more full capacity temporary general purpose processors).

Question:
When can I begin placing On/Off CoD orders against my z9 EC?

Answer:
On/Off CoD can be initiated as soon as the profile for the z9 EC is established. The prerequisite of establishing a profile is the signing of an On/Off CoD contract associated with ordering features 9898 and 9896.

Question:
Will z890 and z990 servers be able to utilize unassigned capacity the same as the z9 EC?

Answer:
No. Though z890 and z990 servers can utilize unassigned PUs, the z890 and z990 have no pricing distinction between unassigned (purchased but turned off) and uncharacterized (not purchased) capacity.
**Question:**
Will I be able to do Capacity Backup Upgrade (CBU) capability on the zIIP?

**Answer:**
Yes. The z9 EC is able to activate most processor types as part of CBU upgrades: IFLs, zAAPs, ICFs, CPs and the zIIPs.

**Question:**
Can I order CBU processors if I have subcapacity processors?

**Answer:**
Yes. CBU is available when you are running with subcapacity processors but you can not decrease the number of installed CP processors. With the full ‘matrix’ upgradeability of the subcapacity general purpose processors you can execute CBU with processors of equal, less or greater capacity depending on your requirements. Note that you can not exceed 8 subcapacity processors (CP or CBU). Interestingly, a customer no longer has to increase the quantity of CPs for CBU, but can just increase the capacity of the existing CP count by ordering CBU CP features with greater capacity. When the quantity of CBU processors exceeds 8, all CBU processors will be full capacity.

**Question:**
Can I add CBU capacity by selecting CBU engines that have more capacity than my permanent configurations and have less actual engines than my original permanent capacity?

**Answer:**
No you can not reduce the number of engines in your permanent configuration when you add CBU capacity.

**Question:**
Can I convert a permanent engine to another engine type during CBU?

**Answer:**
No. All permanent engines must remain as part of the CBU environment (although they may change in capacity) and you can not convert them to another type during the CBU event.
Cryptographic Enhancements

Question:
What common criteria security certifications has IBM System z9 achieved?

Answer:
On March 24, 2006, the IBM System z9 109 (z9-109) joined the ranks of other eServer zSeries servers that have achieved Evaluation Assurance Level 5 for common criteria security certification. The EAL5 ranking should give companies confidence that they can run many z/OS, z/VM and Linux-based applications containing confidential data—such as payroll, human resources, e-commerce, ERP and CRM systems—on one System z platform divided into partitions that keep each applications data secure and distinct from the others. That is, the System z architecture is designed to prevent the flow of information among logical partitions on a system, thus helping to ensure that confidential or sensitive data remains within the boundaries of a single partition.

Question:
What cryptographic capabilities were added to the z9 EC with the April 2006 announcement?

Answer:
Remote key loading is now available for capable Automated Teller Machines (ATM) and Point of Sale (POS) systems. Remote key loading refers to the process of loading DES (Data Encryption Standard) keys and TDES (Triple DES) to ATMs and POS systems from a central administrative site without the need for personnel to visit each machine to manually load keys. In the past, key loading has been done by manually loading each of the two clear text key parts individually and separately into ATMs and POSs. Remote key loading provides a more cost-effective way of managing the terminals than by having several people travel to the ATM and POS system with key parts. This function is supported by z/OS 1.6 and 1.7 with Enhancement to Cryptographic Support for z/OS and z/OS.e 1.6/1.7 Web deliverable (z/OS.e runs on z9 BC only), and z/VM 5.1 and 5.2 for z/OS guests.

Question:
What advantages does remote key loading provide for businesses that manage ATMs and POS systems?

Answer:
By providing a way to load ATM and POS keys without sending staff onsite, remote key loading can reduce downtime due to key errors, reduce service call and key management costs and improve the ability to manage ATM/POS conversions and upgrades.
**Question:**
What ATMs and POS systems will support the IBM System z9 Remote Key Load function?

**Answer:**
IBM System z9 Remote Key Load capability will support ATMs and POS systems that adhere to the following standards:


**Question:**
What is ISO 16609 CBC Mode T-DES MAC enhancement?

**Answer:**
International Standards Organization (ISO) 16609 supports the requirements for message authentication using T-DES symmetric encryption. Integrated Cryptographic Service Facility (ICSF) with Crypto Express2 feature, support basic mechanisms in Remote Key loading. The implementation offers a secure bridge between the highly secure Common Cryptographic Architecture (CCA) environment and the various formats and encryption schemes offered by ATM vendors. Refer to Application Programmers Guide, SA22-7522, for additional details.

**Question:**
What cryptographic hardware is supported on the z9 EC?

**Answer:**
CP Assist for Cryptographic Function (CPACF) on every CP and IFL and the Crypto Express2 feature.

**Question:**
What cryptographic hardware is not supported on the z9 EC?

**Answer:**
PCI Cryptographic Coprocessor (PCICC), PCI X Cryptographic Coprocessor (PCIXCC), CMOS Cryptographic Coprocessor Facility (CCF) and the PCI Cryptographic Accelerator (PCICA) features are not supported on the z9 EC.

**Question:**
Is the cryptographic hardware offered as standard features on the z9 EC?

**Answer:**
The CP Assist for Cryptographic Function (CPACF) is standard on every CP and IFL, however, a no-charge enablement feature #3863 is required. The Crypto Express2 feature is an optional feature. The first order increment is two features.
Question: What features are supported by the CP Assist for Cryptographic Function (CPACF) on the z9 EC?

Answer: CPACF includes support of the Advanced Encryption Standard (AES) for 128-bit keys, Secure Hash Algorithm-256 (SHA-256), and Pseudo Random Number Generation (PRNG).

CPACF, supporting clear key encryption, is activated using a no charge enablement feature #3863 and offers the following on every processor (PU) identified as a general purpose processor (CP) or Integrated Facility for Linux (IFL).

- Data Encryption Standard (DES)
- Triple Data Encryption Standard (TDES)
- Advanced Encryption Standard (AES)
- SHA-1
- SHA-256
- Pseudo Random Number Generation (PRNG)

Performance is designed to scale with the addition of PUs. SHA-1 and SHA-256 are shipped enabled on all servers and do not require the enablement feature. DES, TDES and AES functions require enablement of the CPACF function (Feature Code 3863) for export control. CPACF cryptographic functions are aimed at encryption, decryption and hashing of data transferred over open networks and data sent to storage.

Question: What features are available with Crypto Express2?

Answer: Crypto Express2 is configurable. The Crypto Express2 feature has two PCI-X adapters, and each can be defined as a either a Coprocessor or as an Accelerator

- The Crypto Express2 Coprocessor (default)
  - Supports:
    - Secure key encrypted transactions
    - Supports highly secure cryptographic functions, use of secure encrypted key values, and User Defined Extensions (UDX)
  - Designed for Federal Information Processing Standard (FIPS) 140-2 Level 4 certification.

- Crypto Express2 Accelerator – to enable significant improvement in SSL acceleration on System z9, z890 and z990 servers.
  - Supports clear key RSA acceleration
  - Offloads compute-intensive RSA public-key and private-key cryptographic operations employed in the SSL protocol.
Since the configuration functions are implemented in Licensed Internal Code, current Crypto Express2 features can be carried forward from z990 to the z9 EC to take advantage of the potential for increased SSL performance and configuration capability.

- Up to eight Crypto Express2 features per server.
- All logical partitions (LPARs) in all Logical Channel Subsystems (LCSSs) have access to the Crypto Express2 feature, up to 32 LPARs per feature.

**Question:**
What are the functional differences between the Coprocessor and Accelerator configurations on Crypto Express2 features?

**Answer:**
System z9 provides the ability to configure Crypto Express2 PCI-X adapters as accelerators. When both PCI-X adapters are configured as accelerators, the Crypto Express2 feature is designed to perform up to 6000 SSL handshakes per second. This represents, approximately, a 3X performance improvement compared to the PCICA feature or the current Crypto Express2 feature on z990, on a per PCI-X adapter basis. The SSL rate was achieved with a System z9 environment with four processors and two PCI-X adapters cards (one Crypto Express2, both configured as accelerators), z/OS 1.7 with Cryptographic Support for z/OS 1.6 and 1.7 Web deliverable (will not be available after 5/26/06). It will be replaced by Enhancements to Cryptographic Support for z/OS and z/OS.e 1.6/1.7 Web deliverable (planned to be available May 26, 2006). Also required is ICSF FMID HCR7730.

Since the performance enhancements are implemented in Licensed Internal Code, current Crypto Express2 features that have been carried forward from z990 to System z9 platforms can take advantage of increased SSL performance and configuration capability. These measurements are examples of the maximum transactions per second achieved in a laboratory environment with no other processing occurring and do not represent actual field measurements. Details are available upon request.

Previously, the Crypto Express2 feature was configured as a coprocessor. It was not optimized for SSL performance. Details are available on request.

1 Note, the previously reported SSL performance of 4995 handshakes per second was obtained on a 4-way z990 with four Crypto Express2 Coprocessors (CEX2C) features, whereas in this case the performance was measured on a 4-way System z9 platform with one Crypto Express2 feature with both PCI-X adapters configured as accelerators. It would be expected that the SSL performance on a 16-way System z9 platform with six Crypto Express2 features would be greater than that obtained on a z990, however, actual measurements have not been taken.

**Question:**
Will the Crypto Express2, feature code 0863, support Linux Secure Sockets Layer (SSL) and Transport Layer Security (TLS) cryptographic operations on the z9 EC?

**Answer:**
Yes, it supports Public Key operations with Linux on System z. IBM is working with its distribution partners to provide this function in future distribution releases, or service updates.
**Question:**
What Integrated Cryptographic Service Facility (ICSF) services are available with CP Assist for Cryptographic Function (CPACF)?

**Answer:**
All critical Integrated Cryptographic Service Facility (ICSF) services that currently execute on z890 and z990 PCIX Cryptographic Coprocessor (PCIXCC) feature are planned to be supported by the Crypto Express2 feature.

The following ICSF callable services will be available with CPACF.

CSNBSYE, CSNBSYE1, CSNBSYD, and CSNBSYD1 will provide support for clear-key AES encryption and decryption with 128-bit keys using the CPACF.

CSFNBOWH and CSNBOWH1 will provide support for SHA-1 and SHA-256 using CPACF.

These services continue to support DES and TDES.

**Question:**
What other means are available for customers to utilize CPACF?

**Answer:**
For IBM and customer written programs, CPACF function for DES, TDES, AES, SHA-1 and SHA-256 functions can be invoked by five (5) instructions as described in the z/Architecture™ Principles of Operation, SA22-7832-02. As a group, these instructions are known as the Message Security Assist (MSA). These are all problem state instructions and are all in RRE format.

**Question:**
Is a Trusted Key Entry (TKE) workstation required for the use of the CPACF?

**Answer:**
No, the CP Assist for Cryptographic Function (CPACF) supports clear key functions and does not require entering of master keys.

**Question:**
Will there be an update to the ATS TechDocs Web site to provide additional technical information about System z9 cryptographic hardware features?

**Answer:**
ATS TechDocs Web site and several System z9 cryptographic technical papers will be updated as appropriate. The ATS TechDocs Web site URL is [ibm.com/support/techdocs/atsmastr.nsf](http://ibm.com/support/techdocs/atsmastr.nsf).

**Question:**
What releases of operating systems are required to support the Crypto Express2 hardware feature on the z9 EC?

**Answer:**
The software support requirements for Crypto Express2 and CP Assist for Cryptographic Function (CPACF) features are as follows:

- Crypto Express2:
- z/OS 1.6 and later with Cryptographic Support for z/OS 1.6 and 1.7 Web deliverable (will not be available after 5/26/06). It will be replaced by Enhancements to Cryptographic Support for z/OS and z/OS.e 1.6/1.7 Web deliverable (planned to be available May 26, 2006) and PTFs. Compatibility support is provided on z/OS 1.4 and 1.5. z990 and z890 enhancements to cryptographic support are Web deliverable.
- z/VM 5.1 and later for z/OS and Linux guests, with applicable PTFs.
- VSE/ESA 2.7 and later (z/VSE 3.1 for configurable Crypto Express2)
- Linux for System z9 – IBM is working with its distribution partners to provide this function in future distribution releases, or service updates.

- CPACF (SHA-1, SHA-256, AES and PRNG):
  - z/OS 1.6 and later with Cryptographic Support for z/OS 1.6 and 1.7 Web deliverable (will not be available after 5/26/06). It will be replaced by Enhancements to Cryptographic Support for z/OS and z/OS.e 1.6/1.7 Web deliverable (planned to be available May 26, 2006) and PTFs. Compatibility support is provided on z/OS 1.4 and 1.5. z990 and z890 enhancements to cryptographic support are Web deliverable.
  - z/VM V4.4 and later for guests.
  - z/VSE V3.1, and later, with applicable PTFs.
  - Linux for System z9 – IBM continues to work with its distribution partners to provide this function in future distribution releases, or service updates.

---

**Question:**
Will UDX's written for zSeries servers function on the z9 EC?

**Answer:**
Pre-existing UDXs on current systems may need to be ported to the z9 EC. Customers need to contact IBM to port existing UDXs to the Crypto Express2 environment on the z9 EC. For further information, see the answer to the next question.

---

**Question:**
Will new UDX's be supported on the z9 EC?

**Answer:**
Yes. If you wish to inquire further about UDX support for the Crypto Express2 feature, please contact one of the following IBM representatives: Leo Moesgaard (Leo.Moesgaard@dk.IBM.com) or David Evans (Davee@us.IBM.com)
**Question:**
What are the functions and attributes of the CP Assist for Cryptographic Function (CPACF) and Crypto Express2 features?

**Answer:**
The following table highlights the features or attributes:

<table>
<thead>
<tr>
<th>Functions or attributes</th>
<th>CPACF</th>
<th>Crypto Express 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supports z/OS applications using ICSF</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>SSL handshake capability - performance best when both PCI-X adapters are configured in accelerator mode</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Provides highest symmetric encryption performance (clear key)</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Provides highest symmetric encryption performance (secure key)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Provides highest asymmetric (clear key) encryption performance (in accelerator mode)</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Provides highest asymmetric (encrypted key) encryption performance (in coprocessor mode)</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Uses CHPID numbers</td>
<td>N/A</td>
<td>N/A</td>
</tr>
<tr>
<td>Physically imbedded on each General Purpose Processor (CP)</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Requires CP Assist for Cryptographic Function (CPACF) enablement</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>Requires ICSF to be active, for z/OS users</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>Requires system master keys to be loaded (in coprocessor mode)</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>Offers user programming function support (UDX) (in coprocessor mode)</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>Usable for data privacy - encryption and decryption processing</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>Usable for data integrity - hashing and message authentication</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>Usable for financial processes and key management operations (in coprocessor mode)</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>Crypto performance RMF monitoring</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>System (master) key storage (in coprocessor mode)</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Retained key storage (in coprocessor mode)</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Designed for tamper-resistant hardware packaging (in coprocessor mode)</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Designed for FIPS 140-2 Level 4 certification (in coprocessor mode)</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>Supports SSL functions</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>Supports Linux applications performing SSL handshakes</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>RSA functions</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>High Performance SHA-1, SHA-256 Hash function</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Clear key DES/T-DES</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>Clear key RSA</td>
<td></td>
<td>X</td>
</tr>
</tbody>
</table>
Question: Does the use of RSA Retained private keys limit availability?

Answer: Yes. The use of retained private keys creates an application single point of failure. Since RSA Retained private keys cannot be copied, backed up or scaled from a performance perspective, these keys should only be used if mandated by the customers' security policy. For those customers that require a private key that is intended to be shared across logical partitions, they should use RSA keys encrypted under a host master key instead of a retained key. The use of the RSA keys encrypted under a host master key is designed to prevent the loss of the key associated with the RSA Retained private key specific to the Crypto Express2 feature.

Question: Will there be an upgrade to the Trusted Key Entry (TKE) workstation?

Answer: Yes, a new TKE workstation will be available. The new TKE 5.0 workstation is assigned feature code 0839. Customers must use the TKE 5.0 workstation to control the z9 EC. Customers may continue to use TKE 3.x and 4.x workstations to control prior servers, but an existing workstation cannot be upgraded to TKE 5.0.

Question: Will customers have the option to order Trusted Key Entry (TKE) workstation with Token-Ring or Ethernet?

Answer: No, a TKE workstation with Token Ring is not offered. Ethernet is the only option offered. This satisfies the Statement of General Direction in Hardware Announcement 104-115, dated April 7, 2004, Hardware Announcement 104-117, dated April 7, 2004, and Hardware Announcement 104-118, dated April 7, 2004.

Question: Will Smart card reader support be available with z9 EC TKE workstations?

Answer: Yes. Support is available for an optional Smart Card Reader to be attached to the new TKE 5.0 workstation. Customers may also carry forward optional Smart Card Reader features.

Question: Will the number of TKE workstations be limited to one per system?

Answer: Up to three (3) TKE 5.0 workstations per system will be supported on the z9 EC. This means that up to three (3) smart card reader features, each feature consisting of two smart card readers, can be attached to the z9 EC.
Parallel Sysplex Enhancements

Note: Please use the updated STP FAQs to answer your questions on the new Server Time Protocol feature of the z9 EC. These can be found at: www.ibm.com/systems/z/faq/
GDPS Enhancements

**Question:**
What GDPS functions were announced in January 2006?

**Answer:**
IBM announced extensions to the HyperSwap™ functionality. HyperSwap is the technology within GDPS that designed to allow z/OS and z/VM systems to “switch” to secondary disks in the event of a disk failure or disk maintenance with only a momentary pause in application availability. Virtual servers, (Linux servers for example) running as z/VM guests, can also benefit from GDPS and the extended HyperSwap function. GDPS is adding a new HyperSwap trigger designed to allow tailoring for unplanned HyperSwap conditions when a disk subsystem is experiencing soft failures, such as a non-responsive disk subsystem.

GDPS/HyperSwap Manager within a single Data Center allows GDPS/PPRC HyperSwap Manager configured within a single data center to invoke HyperSwap for individual disk subsystems that need to be switched, helping to simplify systems management and improve availability.

**GDPS Enhanced Recovery Support:** GDPS/PPRC enhancement helps to reduce potentially long or variable recovery times when there is a failover by helping to ensure that the secondary copy of the data at the recovery site and the data in the coupling facility are time consistent. This helps to enable consistent application restart times and reduce the need for special recovery procedures.

**z/OS R7 XRC+ Support:** System Logger is planned to provide new support for XRC+ by allowing asynchronous writes to staging data sets for logstreams improving the throughput for high-volume logging applications, such as WebSphere, CICS, and IMS. The use of asynchronous writes can allow the use of XRC for some application for which it was not previously practical, which may significantly help to improve availability and recovery.

**GDPS/Global Mirror Support:** The latest member of the GDPS suite of offerings, GDPS/Global Mirror offers a multisite, end-to-end disaster recovery solution for your IBM z/OS systems and open systems data using IBM TotalStorage Global Mirror technology.

These functions are available with GDPS V3.3. Contact gdps@us.ibm.com to get more information on GDPS solutions.
Question:
Did the April 27, 2006 announcement contain anything about VSE?

Answer:
The April 27 announcement included important VSE content. IBM previewed a new version of z/VSE. The z/VSE V4 preview introduced two very significant new innovations for VSE.

First, z/VSE V4 will be the first version of VSE to exploit z/Architecture. z/VSE V4 is designed to exploit 64-bit real addressing and support more than 2 GB of processor storage. z/VSE V4 will execute in z/Architecture mode only and will run exclusively on IBM System z servers.

Second, IBM plans to offer attractive new z/VSE V4 pricing for IBM System z. The z/VSE V4 preview contains this Statement of Direction (SOD): “It is IBM's intent to provide new software pricing for z/VSE V4 when running on select processors, subject to applicable terms and conditions. IBM expects this new software pricing metric to provide more granularity and a subcapacity pricing option.”

Note: All statements regarding IBM’s plans, directions and intent are subject to change or withdrawal without notice.

New pricing metrics are expected to provide worthwhile new options for eligible VSE customers. Subcapacity pricing effectively increases the software price granularity available. For example, z/VSE V4 customers on IBM System z9 who select the subcapacity option will pay only for the capacity actually used, not necessarily the total capacity of the server installed. That means z/VSE V4 customers may be able to install more capable IBM System z9 servers than they might have selected otherwise.

Question:
When will z/VSE V4 become available? What will the new price points be?

Answer:
That information is not available at this time. Previews do not contain specific dates or pricing information. Previews are provided to help customer in their IT planning. Of course, any statements regarding IBM’s future direction or plans are subject to withdrawal or change without notice.

Question:
Will I be able to run z/VSE V4 on my IBM System/390 9672 G6 processor?

Answer:
No. z/VSE V4 is designed to run in z/Architecture mode. It will run exclusively on IBM System z servers. IBM System z includes IBM System z9, z990, z890, z900, and z800 servers. Unlike today’s z/VSE V3, z/VSE V4 will not run on Multiprise® 3000 or System/390 G5/G6 processors.
**Question:**
What about migration? Will it be difficult to migrate to z/VSE V4?

**Answer:**
It is reasonable to believe that migration to z/VSE V4 will be relatively straightforward. Support for z/Architecture is designed to be transparent to systems and application programs. In addition, the Fast Service Upgrade (FSU) process will be supported from z/VSE V3 and VSE/ESA V2.7.

**Question:**
Can I get new pricing on z/VSE V3? If I install z/VSE V4 on my z800, can I get the new pricing?

**Answer:**
No. The new pricing metrics apply exclusively to z/VSE V4 on IBM System z9. Other versions of VSE (for example, z/VSE V3 or VSE/ESA V2.7) will continue to be priced as today. z/VSE V4 running on other servers (i.e. z990, z890, z900, and z800) is expected to be priced much like z/VSE V3 is priced today.

**Question:**
Is the z/VSE V4 Preview all that was announced for VSE?

**Answer:**
No. z/VSE V3 supports some important new IBM System z technology. z/VSE V3 (and later) supports CPACF enhancements, Crypto Express2 in both accelerator and coprocessor mode, and N_Port ID Virtualization. In addition, VSE/ESA V2.7 (and later) supports FICON Express4, OSA Express2 1000 BASE-T ethernet, and up to 60 LPARs.
Question: What additional features are being announced for z/VM on April 27, 2006?

Answer: The new additional features being added to z/VM V5.2 with the PTF for APAR VM63952 to exploit the z9 EC and the z9 BC plus other enhancements include:

- Improved memory management for Linux guests with the PTF for VM63856
- Exploitation of selected features of the z9 EC and the z9 BC, including:
  - Support for new instructions
  - Support for FICON Express4 (4 Gbps FICON)
  - Support of HiperSockets using IPv6 protocol by TCP/IP and guest LAN
  - Simplified VLAN management with support for GVRP
- Enhancements for FCP N_Port ID virtualization (NPIV)
- Additional exploitation of the IBM DS6000 and DS8000 series
- Support for Parallel Access Volumes (PAVs) as minidisks
- Ease-of-use enhancement for the Performance Toolkit for VM

Question: How will these new enhancements be made available and when?

Answer: These enhancements are planned to be delivered as a CORrective PTF in the z/VM service stream with the PTFs for APARs VM63952 and VM63856. The PTF for APAR VM63952 is planned to be available on May 26, 2006 and the PTF for APAR VM63856 is planned to be available on June 30, 2006.

Question: Was there any announcement regarding z/VM support for the IBM TotalStorage DS4000 Series Midrange Disk Systems?

Answer: Yes. The z/VM V5.2 support for the IBM DS4000 series midrange disk systems as announced in Software Announcement 205-168, dated July 27, 2005, is being withdrawn, effective immediately. z/VM V5.2 continues to support both the IBM TotalStorage DS6000 series and the IBM TotalStorage DS8000 series on System z servers configured for ECKD and SCSI use to meet the data requirements for z/VM, including the Linux on System z environment or with z/OS, z/OS.e, z/TPF, and z/VSE. The DS6000 is designed and priced to support lower total cost of ownership and provide a highly-available, robust storage solution for System z environments that do not require the full scalability of the DS8000.
**Question:**
Has z/VM been evaluated for common criteria security certification?

**Answer:**
Yes. On October 26, 2005, the German Federal Office of Information Security (Bundesamt für Sicherheit in der Informationstechnik, BSI) issued its certification that z/VM V5.1 conforms to the requirements of the Controlled Access Protection Profile (CAPP) and the Labeled Security Protection Profile (LSPP), both at Evaluation Assurance Level 3+.

**Question:**
Will V5.1 be available for ordering after the availability of V5.2?

**Answer:**
Yes. z/VM V5.1 and its priced, optional features of DirMaint™, RACF®, and the Performance Toolkit for VM™ will continue to be orderable after the availability of z/VM V5.2. However, z/VM V5.1 and its priced, optional features, DirMaint, RACF, and the Performance Toolkit, should only be ordered if you require a z/VM release with Common Criteria (ISO/IEC 15408) certification against the Labeled Security Protection Profile (LSPP) and the Controlled Access Protection Profile (CAPP), both at the EAL3+ assurance level. The Recommended Service Update (RSU) Level 5103RSU (PTF UM97510) plus the RACF for z/VM PTF for APAR VM63613 is required to maintain certification. If you apply any additional service to z/VM V5.1, other than PTF UM97510 and the PTF for RACF APAR VM63613, you no longer will have a certified system. z/VM V5.1 and its priced, optional features of DirMaint, RACF, and the Performance Toolkit for VM are planned to be withdrawn from marketing on September 30, 2007 and to be supported until September 30, 2007.

**Question:**
Where can I get more information on these new enhancements for z/VM V5.2?

**Answer:**
There is a complete set of Frequently Asked Questions for z/VM planned to be available on the z/VM Web site at: [www.vm.ibm.com/zvm520/](http://www.vm.ibm.com/zvm520/)
IBM Global Financing (IGF)

**Question:**
How does financing fit with the on demand capabilities of the System z9 products?

**Answer:**
IGF can finance the two capacity on demand offerings — Capacity Upgrade on Demand and On/Off Capacity on Demand. IGF financing can assist customers to more easily acquire incremental server resources in response to demand changes.

**Capacity Upgrade on Demand financing**
When customers know they're going to need additional capacity in the future, but don't know when, Capacity Upgrade on Demand financing can provide the perfect balance of flexibility and affordability. Customers get the ability to activate pre-installed additional processors at any time during their original lease term. An extension to the original lease term keeps payments low and reflects the longer life expectancy of the system. The cost of the upgrade is rolled in as an affordable addendum lease, so the customer gets all the performance and capacity benefits of a higher-capacity system, for just a modest increase to their monthly payment.

**On/Off Capacity on Demand financing**
With On/Off Capacity on demand financing, your customer's system is installed with both activated and inactivated processor capacity. The customer pays a monthly fee over a fixed term for only the base capacity activated at the time of installation.

When additional capacity is turned on and off as needed, they pay an additional usage fee for the incremental increase in capacity, billed in processor-days (with a minimum increment of one processor-day). These additional fees can be assessed as one-time charges, or financed on a short-term revolving type of credit arrangement for a period of twelve months, so the customer can "smooth out" the billing for peak capacity to a more manageable payment structure. At the end of the short-term financing period the remaining balance, if any, can be paid off in one lump sum, or included in a recalculated lease extension for the remaining term of the base agreement, or rolled into a new term.

**Question:**
What capabilities exist for financing non-HW items that are part of a business transformation project utilizing a System z9?

**Answer:**
IBM Project Financing is a single-source financing solution for large-scale, multivendor business transformations that can include consulting services, infrastructure investment and business process implementation. Highly customizable structures can be tailored to even the most complex project plans, and can cover the entire project's life cycle. IBM Project Financing facilitates project approval, preserves cash flow and credit lines, and lets you better match costs to projected benefits to enable self-funding.
**Question:**
What about financing non-hardware items like this outside the scope of a large project?

**Answer:**
IGF can finance all hardware, software and services in a single contract.

---

**Question:**
How do I engage IBM Global Financing?

**Answer:**
The first step is to contact your IBM Global Financing representative. If you don't already have a representative, please click here: "ibm.com/financing/servers" to find one.

---

**Question:**
Where can I find current IBM Global Financing pricing information?

**Answer:**
Please contact your IBM Global Financing representative for complete pricing information. If you don't already have a representative, please click here "ibm.com/financing" to find one.
Fiber Cabling for your IBM System z9

Question:
There is a broad range of connectivity on the System z platforms. How can I help to be sure I have the right cables to attach to my infrastructure?

Answer:
We agree — there is a wide variety of connectivity. Also, the fiber requirements are changing and getting more complex. In order to help manage the complexity and plan your environment, cables attaching your mainframe to your IT infrastructure zSeries fiber cabling service and enterprise fiber cabling offered are available from IBM Global Services. These services offerings allow you to plan for your environment as a whole instead of product by product. Without a cabling strategy for your data center you may spend unnecessary time and money initiating changes. The zSeries Fiber Cabling Service helps you effectively manage your connectivity needs on your zSeries and System z9 platforms.

Question:
Will there be a specific System z9 Fiber Cabling Service offering?

Answer:
No. The zSeries Fiber Cabling Service will support both the zSeries and the System z9 platform.

Question:
Why is IBM offering the zSeries fiber cabling service offerings?

Answer:
The complexity of specifying fiber cabling connectivity has increased dramatically over the past ten years because of new I/O function, higher bandwidth, significant growth in fiber cabling technology, a migration to small form factor connectors, and the emergence of new cabling standards. The number of cabling options now requires detailed planning. A customer must make cabling decisions based on goals that best fit their business and environment. The combination of the right IBM fiber cabling service offering and trained cabling specialists can help resolve the cabling issues and find the right solution for the customer’s server installation.

Question:
What are the zSeries fiber cabling services options?

Answer:
The zSeries fiber cabling service offering has three options to choose from. Each option has been created to help handle different customer situations for planning and managing individual jumper cable situations.

- Option 1 is a comprehensive fiber optic jumper cable package offering fiber cable planning, new cables, installation and documentation. This option applies to new server installations as well as server upgrades where either an entire new set of fiber cables is desired or an add of I/O ports is part of the upgrade.
- Option 2 is a fiber cable migration offering which can be used when reusing existing fiber cables in a server upgrade scenario. This option provides for the planning, relabeling, moving and documentation of existing fiber cables to the new system.

Option 1 (new cables) and Option 2 (reused cables) will be frequently used together for a typical server upgrade scenario. Option 1 and 2 are included in a single services contract for ease-of-use and fast deployment.

- Option 3 provides new cables and installation in situations where the fiber cable planning has been completed and a cable list is already available. Option 3 is always a separate IBM Networking Services contract.

Fiber cables are available for ESCON, FICON, OSA, ISC-3, Sysplex Timer and ETR.

**Question:**
Can I use my currently installed IBM jumper cables with the System z9 platform?

**Answer:**
The answer, today, is “maybe” depending on the application, the customer upgrade strategy, the connectors supported on the system and on the devices, as well as the customer cabling strategy. There are also other alternatives that should be considered even if reuse of existing cables is possible. For example, new jumper cables might be more desirable than reusing existing cables with two meter conversion kits. New jumper cables with the correct fiber connectors on both ends eliminates one connection in the optical path (link reliability), avoids the 2 meter cable/connector “mess” immediately under the server floor (manageability), can help eliminate the stress on the connector when moving the cable (cable failure), and helps provide for easier connectivity changes (faster upgrade and change times). Similarly, using MCP cables when reusing multimode fiber optic cables with long wavelength (LX) transceivers can facilitate the use of existing fiber optic cables but may impact less loss budgets and may be affected by link data rates. (Note that MCP cables are not supported for data rates over 1 Gbps link data rates only). Another alternate solution is an enterprise fiber trunking solution to help make the fiber infrastructure even more useable, changeable, upgradeable and manageable. Today, the customer needs a fiber cabling strategy to guide decisions and provide a workable infrastructure. IBM Networking Services can help.

**Question:**
I have heard about a structured fiber optic cabling infrastructure. What is this?

**Answer:**
A structured fiber cabling environment means fiber optic trunks where, for example, up to 72 FICON channel fiber cables can be contained in approximately a one-inch trunk. Other hardware would include patch panels, patch cables (between patch panels), under floor boxes, fiber cable harnesses (six channels per harness), mounting brackets and associated hardware are involved. A structured infrastructure may eliminate the hundreds (thousands) of jumper cables that may currently be in the data center. It is a very manageable infrastructure which moves cabling problems away from the server or switch and into the patch panel which, by its very nature, allows flexible change. FICON is the key protocol being trunked today. Key words for a structured environment are useable, changeable, upgradeable, and manageable. Any data center with two or more servers should consider a structured infrastructure.
**Question:**
I have heard about a structured fiber optic cabling infrastructure and the standard TIA-942. What is this?

**Answer:**
The purpose of the TIA-942 Telecommunications Infrastructure Standard for Data Centers is to provide guidelines for the design of a data center or computer room. It is intended for use by designers who need a comprehensive understanding of data center design, including facility planning, the cabling system, and network design. Structured cabling is one of the design elements of this standard.

**Question:**
What are the enterprise fiber cabling service options?

**Answer:**
The enterprise fiber cabling service offering has two options to choose from. Each option has been created to handle a different customer situation for planning and managing individual trunked cable situations.

- **Option 1** is the zSeries fiber optic trunk cabling package. This option offers a trunking solution for a single System z platform being installed into an already structured (fiber trunk) environment. This option includes System z planning, fiber optic trunk commodities, installation, and documentation. It should be noted that the planning, procurement and installation of individual jumper cables, if required for the server installation, can also be included with this option which allows all fiber connectivity requirements to be fulfilled in a single IBM Networking Services contract.

- **Option 2** is the enterprise fiber cabling option which was formally called Fiber Transport Services. This option is designed to provide a custom structured (trunked) solution for small, medium or large enterprises. Whereas the zSeries fiber optic trunk cabling package was limited to a single zSeries server, this option will provide a fiber optic infrastructure for the entire data center allowing the interconnection of servers, switches, and devices. This option includes fiber optic trunk planning, fiber optic trunk commodities, installation, and documentation for the data center. The planning, procurement and installation of individual jumper cables, if required, can also be included with this option which allows all fiber connectivity requirements to be packaged into a single IBM Networking Services contract.

**Question:**
Will there be a separate System z9 Fiber Optic Trunk Cabling Package for the z9 EC?

**Answer:**
No. Customers should use the zSeries Fiber Optic Trunk Cabling Package when they need a trunking solution for a single z9-109.
**Statements of Direction**

All statements regarding IBM's plans, directions, and intent are subject to change or withdrawal without notice.

**Question:**
What statements of direction has IBM made for Server Time Protocol in the October 10th announcement?

**Answer:**
There were two statements of direction in the announcement for STP:

1. **Enhanced Coordinated Server Time Accuracy:** IBM intends to enhance the accuracy of initializing and maintaining Coordinated Server Time to an international time standard such as Coordinated Universal Time (UTC). The then current server is planned to have the capability of attaching to an external time source, such as a Global Positioning System (GPS) receiver.

2. **Network Time Protocol (NTP) client support:** IBM intends to enhance the STP design to provide Network Time Protocol (NTP) client capability, so that Coordinated Server Time may be initialized and maintained to time provided by an NTP server. The purpose of this function is to allow customers to have the same time across an enterprise comprised of heterogeneous platforms.

**Question:**
What statements of direction did IBM make on July 27, 2005 for future z/VM support on the System z9 platform?

**Answer:**
IBM intends to provide future enhancements to z/VM supporting the following System z9 functions:

- System and guest exploitation of HiperSockets IPv6
- Improved memory management between z/VM and Linux on System z
- Simplified networking administration and management of VLANs with support for GARP VLAN Registration Protocol (GVRP) using OSA-Express2
- Capability to allow guest operating systems and z/VM users to query virtual port names when using N_Port ID Virtualization

These statements of direction are planned to be satisfied with the PTF for APAR VM63952 on May 26, 2006.
Question:
What statements of direction has IBM made for IBM TotalStorage?

Answer:
IBM intends to provide the following enhancements:

- **IBM TotalStorage DS8000 Series of disk products**

  These enhancements for use in the System z9 environment include:
  - IBM TotalStorage z/OS Global Mirror (XRC) and IBM TotalStorage Global Mirror (asynchronous PPRC) capability on the IBM TotalStorage DS8000 Series to enable the use of consistency groups that can span both remote mirroring products for enhanced business continuity. This capability is intended to help customers maintain and leverage their investment in z/OS Global Mirror (XRC) while implementing Global Mirror (asynchronous PPRC) for heterogeneous server environments.
  - A continuous data protection function on the IBM TotalStorage DS8000 Series for System z9 environments. This capability is intended to provide support for enhanced data resiliency and business continuity.

**IBM TotalStorage SAN Volume Controller (SVC):** To enhance the interoperability of the IBM TotalStorage SAN Volume Controller, IBM is planning to provide SVC support for Linux on System z9 environments. This is designed to allow System z9 hosts and supported open system hosts to share storage pools created by the SAN Volume Controller.

**IBM TotalStorage Virtual Tape Server (VTS) - business continuity enhancements:**

- Provision of an enhanced Import/Export function designed to allow sets of cartridges to be interchanged between VTSs and/or Peer-to-Peer (PtP) VTSs or exported for storage in a remote vault. This enhancement is designed to allow virtual volumes to be directed to a VTS volume pool, subsequently exported on physical cartridges under host control, and imported into another VTS under host control.
- Extension of the PtP VTS - support for full-duplex virtual volume replication between three sites. DFSMS parameters are designed to provide policy-based copy management to control the source and target VTSs, the preferred routing, and the mode of operation.

**IBM TotalStorage SAN File System (SFS):** SFS was designed to help you manage the growing complexities and rising costs associated with dramatic growth in disk storage requirements. IBM continues to expand on the concept of system-managed storage by working to extend some of the storage management capabilities of DFSMS to open system environments to support improvements in storage asset utilization and administrator productivity.

IBM plans to increase the range of IBM TotalStorage SAN File System supported hosts to include the 31-bit SUSE Linux Enterprise Server 8 (SLES 8) client distribution running under z/VM on System z9 and zSeries. This support is designed to apply exclusively to hosts acting as the SFS client in a System z9 or zSeries server running SUSE SLES 8. This extension of host support for SFS is intended to support increased data sharing between UNIX, Linux, Microsoft® Windows®, and mainframe platforms.
What statements of direction has IBM made for ICB-3?

**Answer:**
IBM intends to phase out the Integrated Cluster Bus-3 links (ICB-3 links) over time. IBM plans to support ICB-3 links through the lifecycle of the System z9 platform.

**Question:**
Did the April 27, 2006 announcement contain anything about VSE?

**Answer:**
The April 27 announcement included important VSE content. IBM previewed a new version of z/VSE which contains this Statement of Direction (SOD): “It is IBM's intent to provide new software pricing for z/VSE V4 when running on select processors, subject to applicable terms and conditions. IBM expects this new software pricing metric to provide more granularity and a subcapacity pricing option.”

New pricing metrics are expected to provide worthwhile new options for eligible VSE customers. Subcapacity pricing effectively increases the software price granularity available. For example, z/VSE V4 customers on IBM System z9 who select the subcapacity option will pay only for the capacity actually used, not necessarily the total capacity of the server installed. That means z/VSE V4 customers may be able to install more capable IBM System z9 servers than they might have selected otherwise.
z/VSE can execute in 31-bit mode only. It does not implement z/Architecture, and specifically does not implement 64-bit mode capabilities. z/VSE is designed to exploit selected features of IBM z9 and zSeries hardware.