



India Systems & Technology LAB - (Technology. Engineering & Solutions)



IBM India Systems and Technology Lab (ISTL) is looking for graduate applicants who are interested in pursuing a career in research. It is open to overseas Indian students who wish to come back to work in India.

Degrees needed: MS/M.Tech. or Ph.D

Majors considered: Electrical Engineering or Computer Science
Qualified candidates must also have relevant domain experience.

NANO KERNEL ENGINEER: Analyze and develop nano-kernel for validating new advances in IBM processors. Responsibilities include development, testing, validation and performance analysis of processors and nano-kernal used for the hardware validation.

SRAM/ IO DESIGN R&D ENGINEER: Develop circuit designs for SRAM, Register files and IO blocks. Responsibilities include development, verification and interface with the other geographies for ASIC/ Processor development.

CELL LIBRARY DEVELOPMENT ENGINEER: Develop BLAS, LAPACK and Mathematical library routines for the Cell Processors. Responsibilities include development, verify cation, analysis and performance optimization of library routines.

IP MAINTENANCE LEAD: Enhancement and maintenance of existing ASIC and Processor IPs and porting them to new and evolving technologies. Responsibilities include leading a team of IP maintenance engineers, interfacing with customers and managing requirements, schedules and deliverables.

IP TEST LEAD: Testing of existing ASIC and Processor IPs and porting the verification platform to new and evolving technologies. Responsibilities include leading a team of IP test engineers, interfacing with customers and managing requirements, schedules and deliverables.

LOGIC DESIGN/VERIFICATION ENGINEER: Development and verification of Architecture, High level design and front end RTL development of ASIC/ Processor modules. Responsibilities include Development, reviews, verification and interfacing with customers.

LOGIC DESIGN/VERIFICATION LEAD: Development and verification of Architecture, High level design and front end RTL development of ASIC/ Processor modules. Responsibilities include leading a team of Logic design and verification engineers, reviews, interfacing with customers and managing requirements, schedules and deliverables.

PROCESSOR CUSTOM LAYOUT ENGINEER: Design and verification of full custom layouts for processor modules. Responsibilities include development of full custom layouts and interfacing with circuit design teams in multiple geographies.

Sr. PROCESSOR CUSTOM LAYOUT ENGINEER: Design and verification of full custom layouts for processor modules. Responsibilities include leading a development team of custom layout engineers, reviews, interfacing with circuit design teams in multiple geographies and managing requirements, schedules and deliverables.

PROCESSOR SEMI CUSTOM LAYOUT ENGINEER: Design and verification of layouts for processor modules using semi custom tool flow. Responsibilities include development of macro layouts, interfacing with circuit design and tool development teams in multiple geographies.

Sr. PROCESSOR SEMI CUSTOM LAYOUT ENGINEER: Design and verification of layouts for processor modules using semi custom tool flow. Responsibilities include leading a development team of semi custom layout engineers, reviews, interfacing with circuit design and tool development teams in multiple geographies and managing requirements, schedules and deliverables.

ASIC DEVELOPMENT LEAD/ MANAGER: Development of EDA processes and methodologies for ASIC development. Responsibilities include leading and managing a development team of ASIC flow development engineers, reviews, interfacing with customers and managing requirements, schedules and deliverables.

CHIP PD LEAD/ MANAGER: Development of complete ASIC from net-list to GDS II. Responsibilities include leading and managing a development team of ASIC physical design engineers, reviews, interfacing with customers and managing requirements, schedules and deliverables.

Application Procedure:

Please send your resume to pkulseja@in.ibm.com and bluevip@in.ibm.com with the job title as subject line

Please note that all jobs compensate according to the local market and will require work authorization. You will be contacted for next steps, if we determine a strong match.

For more information on careers at IBM India

Software Lab visit us at:

www.ibm.com/in/isl/index.html



IBM's Employment Policy

IBM is committed to creating a diverse environment and is proud to be an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, gender, gender identity or expression, sexual orientation, national origin, genetics, disability, age, or veteran status.